Accelerating The Creation Of Semiconductors

Corporate Overview

Charlie Janac
CEO

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August 2026

ARTERIS IP

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In addition to the financials presented in accordance with U.S. generally accepted accounting principles ("GAAP"), this presentation includes the following non-GAAP metrics: non-GAAP operating expenses, non-GAAP operating income (loss) and free cash flow. Non-GAAP metrics have limitations as analytical tools and you should not consider them in isolation or as a substitute for or superior to the most directly comparable financial measures prepared in accordance with U.S. GAAP. There are a number of limitations related to the use of non-GAAP metrics versus their nearest GAAP equivalents. Other companies, including companies in our industry, may calculate non-GAAP metrics differently or may use other measures to evaluate their performance, all of which could reduce the usefulness of our non-GAAP metrics as tools for comparison. We urge you to review the reconciliation Arteris' non-GAAP metrics to the most directly comparable GAAP financial measures, and not to rely on any single financial measure to evaluate our business. See the Appendix for reconciliation between each non-GAAP metric and the most comparable GAAP measure.

Arteris is unable to provide a reconciliation of certain non-GAAP guidance metrics in this presentation because the corresponding GAAP measures are not accessible on a forward-looking basis. Due to the potential variability and limited visibility of the excluded items, providing such reconciliation would necessitate unreasonable effort.

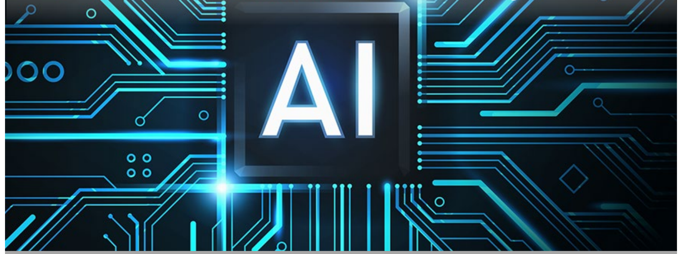
This presentation shall not constitute an offer to sell or the solicitation of an offer to buy securities, nor shall there be any sale of securities in any state or jurisdiction in which such offer, solicitation or sale would be unlawful prior to registration or qualification under the securities laws of any such state or jurisdiction.

Our Future is Based on Semiconductors

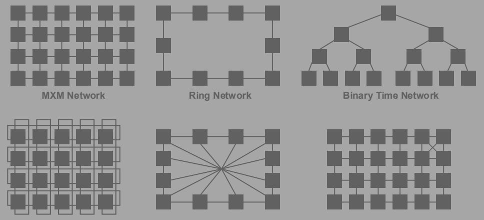
Dramatic expansion of AI workloads in Data Center and the Edge

Adoption of New Semi. Technologies

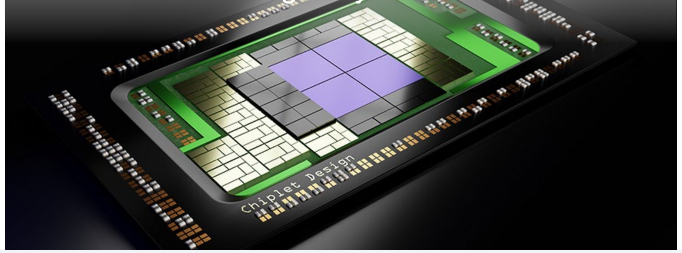
Artificial Intelligence Everywhere
– But won't look like today



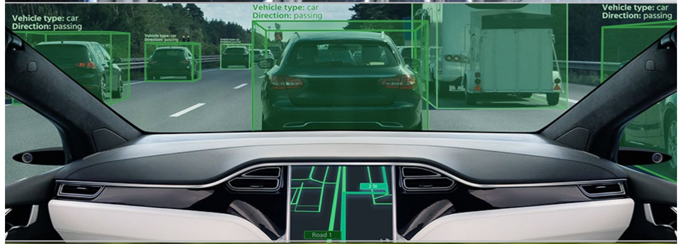
More Processors & IP Blocks
– data movement key competency –



Chiplets
– multi-die SoCs go beyond Moore's Law –



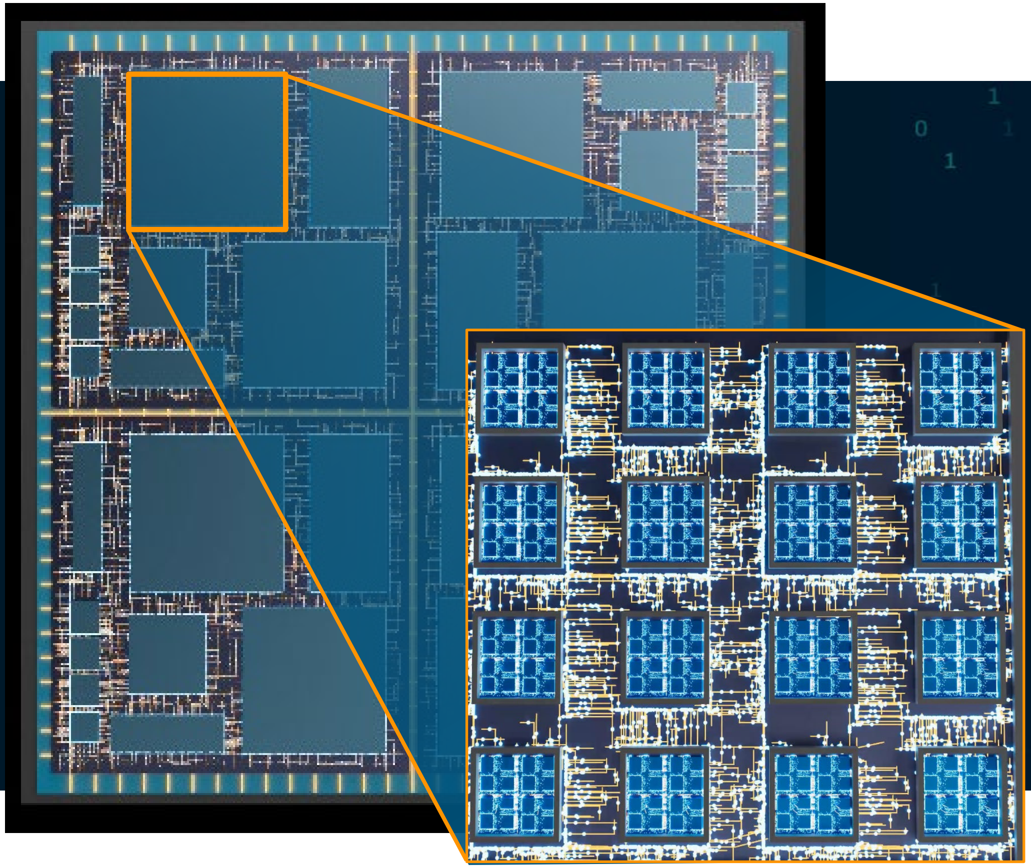
Supporting Silicon Based Business Creation



Supporting Silicon Based Emerging Businesses

Semiconductors Process Data, Store Data and Move Data

Arteris Optimizes High Performance, Efficient, Safe and Secure Silicon Data Movement



Processors & Logic → Data Processing

Memory → Data Storage

System IP → Data Movement

Optimizing data transport is critical to function, performance & power efficiency

https://vimeo.com/1163376621/dc30108e1f

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Arteris Optimizes and Secures Data Movement Across Markets

Arteris Product Lines

Network-on-Chip IP
- Cache coherent NoC IP
- Non-coherent NoC IP
- Data movement options

SoC Integration Software
- IP Block Packaging
- SoC & Chiplet integration
- HW-SW integration

Cybersecurity Software
- Static analysis
- IP Block and subsystem
- SoC and Firmware

ARTERIS IP

Arteris – Connecting Innovation for Silicon Success

SILICON-PROVEN SYSTEM IP

4 billion+ units shipped to date[1]

CONNECTING INNOVATION

975+ SoC design starts to date[1]

INNOVATIVE TECHNOLOGY

148 patents and 138 patent applications[1]

SWITZERLAND OF IP

Any processor, any IP, any EDA, any foundry

BROAD ADOPTION

Used by 9 out of top 10 semiconductor companies

CUSTOMER SATISFACTION

Customer retention rate of ~90%[2]

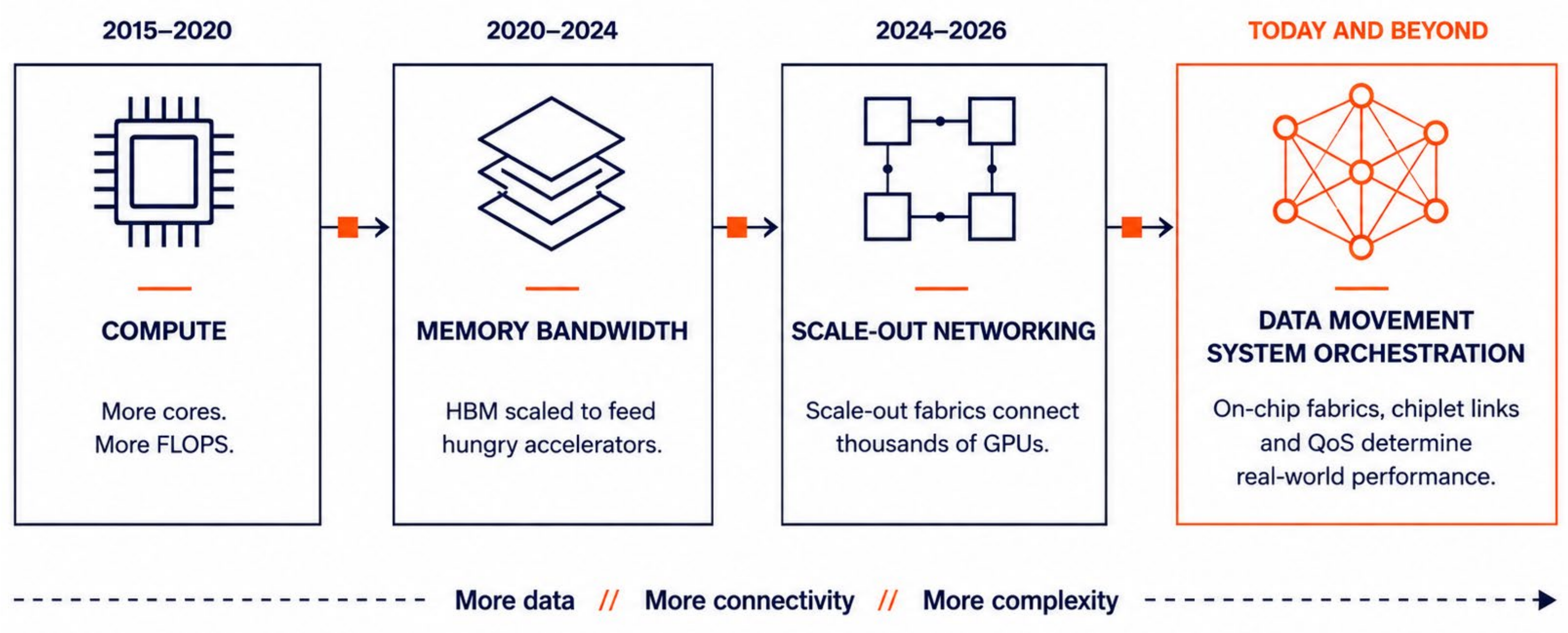
Diversified Customer Base

Subset of Publicly Disclosed Customers

[1] As of June 30,2026
[2] Over a three year period ending June 30, 2026

The Evolution of AI Bottlenecks

AI scale has shifted the limiting factor



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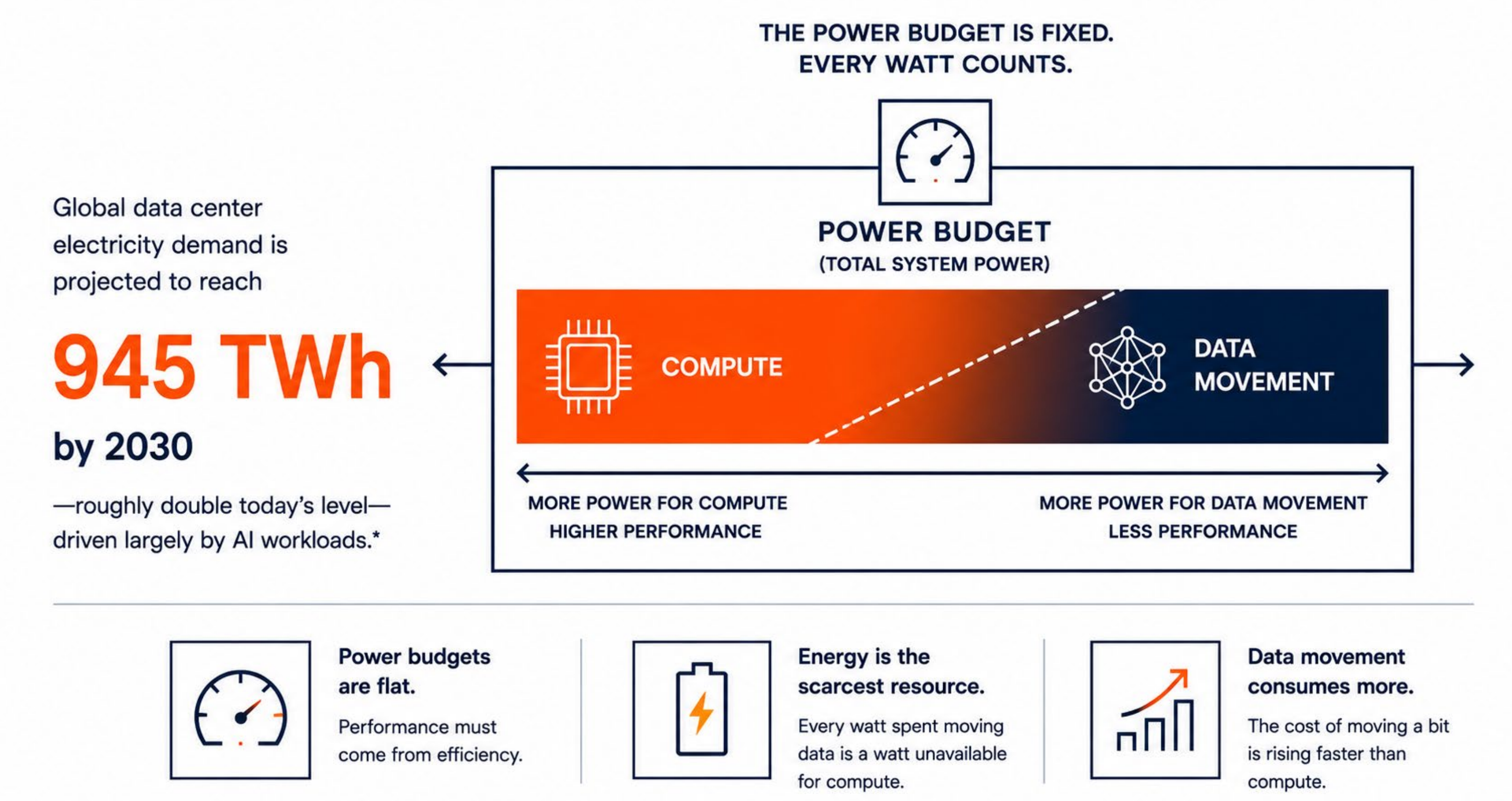
Power is a Major Constraint: Heavily Determined by Data Movement

AI infrastructure is increasingly limited by available power, not just available compute



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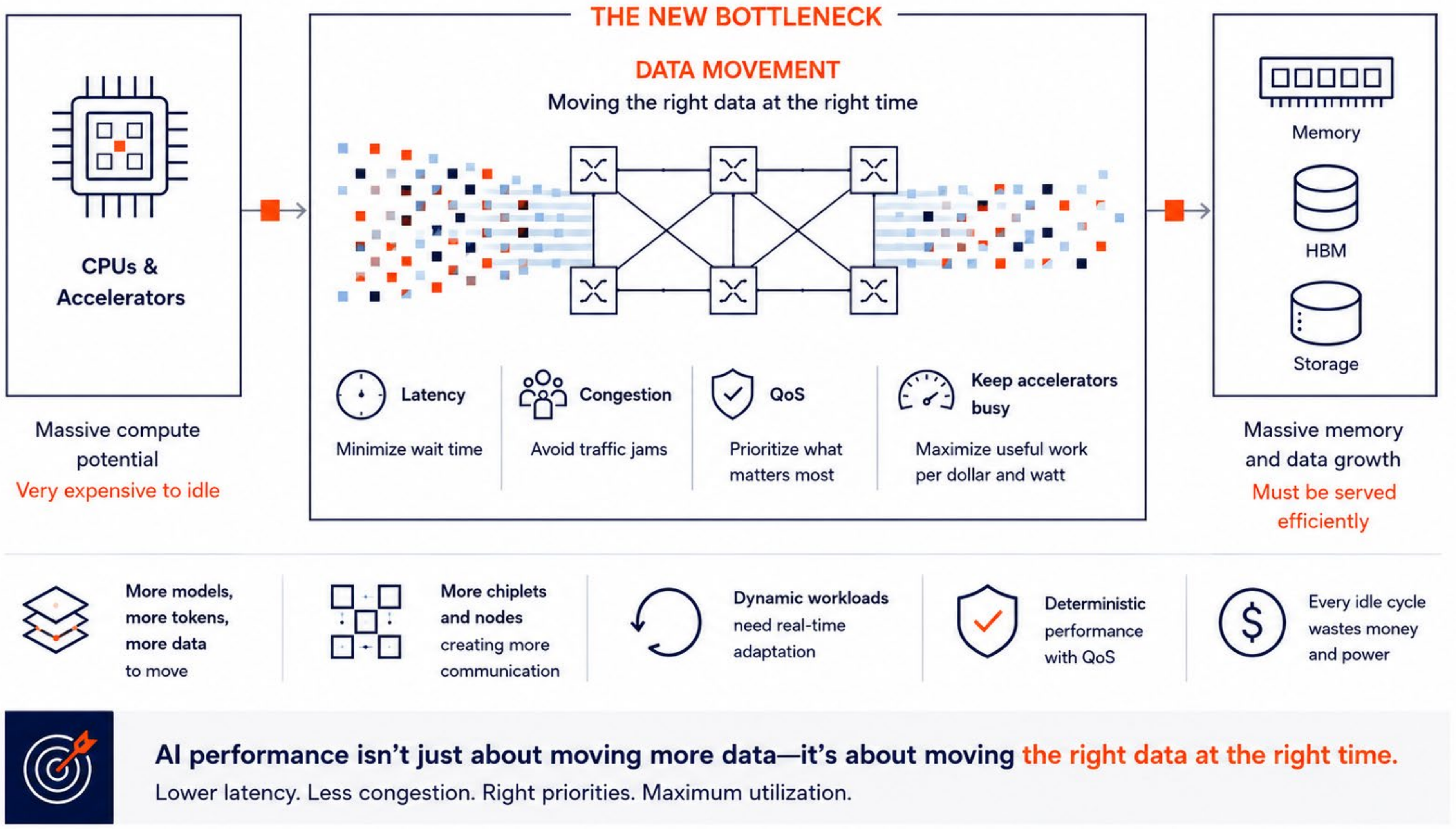
The Bottleneck is Shifting

Performance constrained by moving data at the right time, not just generating compute



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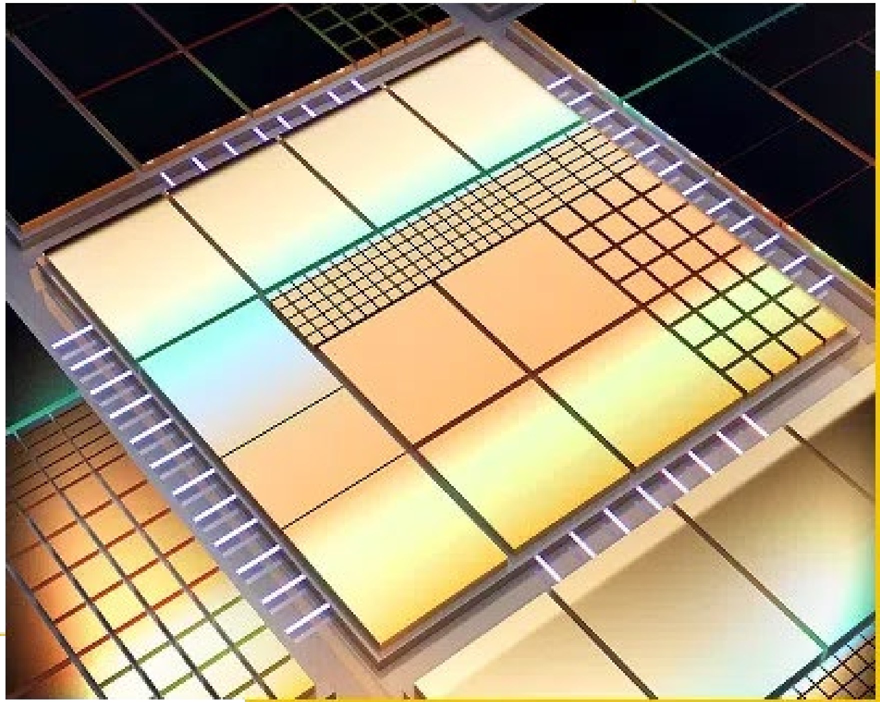
Arteris Expanding Multi-Die Solution to Meet Demand for Chiplets

- **Challenge:** With the rise of AI, computational power requirements are exceeding what is possible through traditional monolithic die designs.
- **Solution:** Arteris Multi-Die Solution delivers backbone technology for rapid chiplet-based innovation across all markets.
- **Ecosystem:** Collaboration for Multi-Die Physical Integration and Interoperability

arm AMBA | UCIe Universal Chiplet Interconnect Express | SYNOPSYS | cadence



Attacking Hardware Used to Take a Genius – Now it Takes an API Key

2018 – to invent SPECTRE

~10 researchers

roughly **7** institutions

10+ years of side-channel science behind it

Spectre disclosure – arXiv:1801:01203

COST COLLAPSE

2026 – to rebuild SPECTRE

$1.25 model-usage

< 4 minutes

to recreate the attack

uGEN – arXiv:2605.15503 (May 2026)

Attack discovery used to be expert work – but that is changing fast.
Attacks are now automated at scale. Spectre showed software can attack hardware.

Market Driver for Semiconductor Cybersecurity

Increasing regulations and standards across sectors

Aerospace & Defense
- 2022 CHIPS Act
- CISA HBOM Framework

Increased national security focus on semiconductor security

Automotive
- ISO/SAE 21434
- UNECE R#155

Industry standards for HW & SW driving need for compliance

Data Centers
- Side Channel attacks
- Date leakage between tenants
- PSA Certification

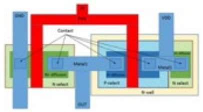
Emerging standards in other industries driven by specific needs

Legislation
- EU Cyber Resilience Act (CRA)
- EU Radio Equipment Directive (RED)

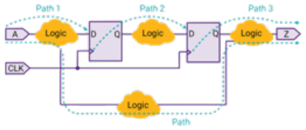
EU legislation spurring action towards cybersecurity assurance

New Chip Signoff Technologies Historically Adopted Over 4-6 Years

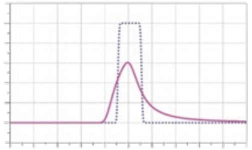
1980-1985 **Design Rule Signoff:**
- Ensure chip mask set works as intended



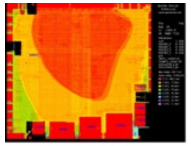
1990-1995 **Timing Signoff:**
- Ensure chip runs at expected speed



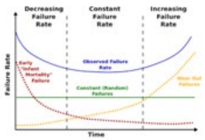
1999-2004 **Signal Integrity Signoff:**
- Ensure chip signals are intelligible



2005-2010 **Power Signoff:**
- Ensure chip does not exceed power budget



2014-2019 **Reliability Signoff:**
- Ensure chip is reliable under stress



2025-2029 **Security Signoff:**
- Ensure chip is secure from cyber attacks

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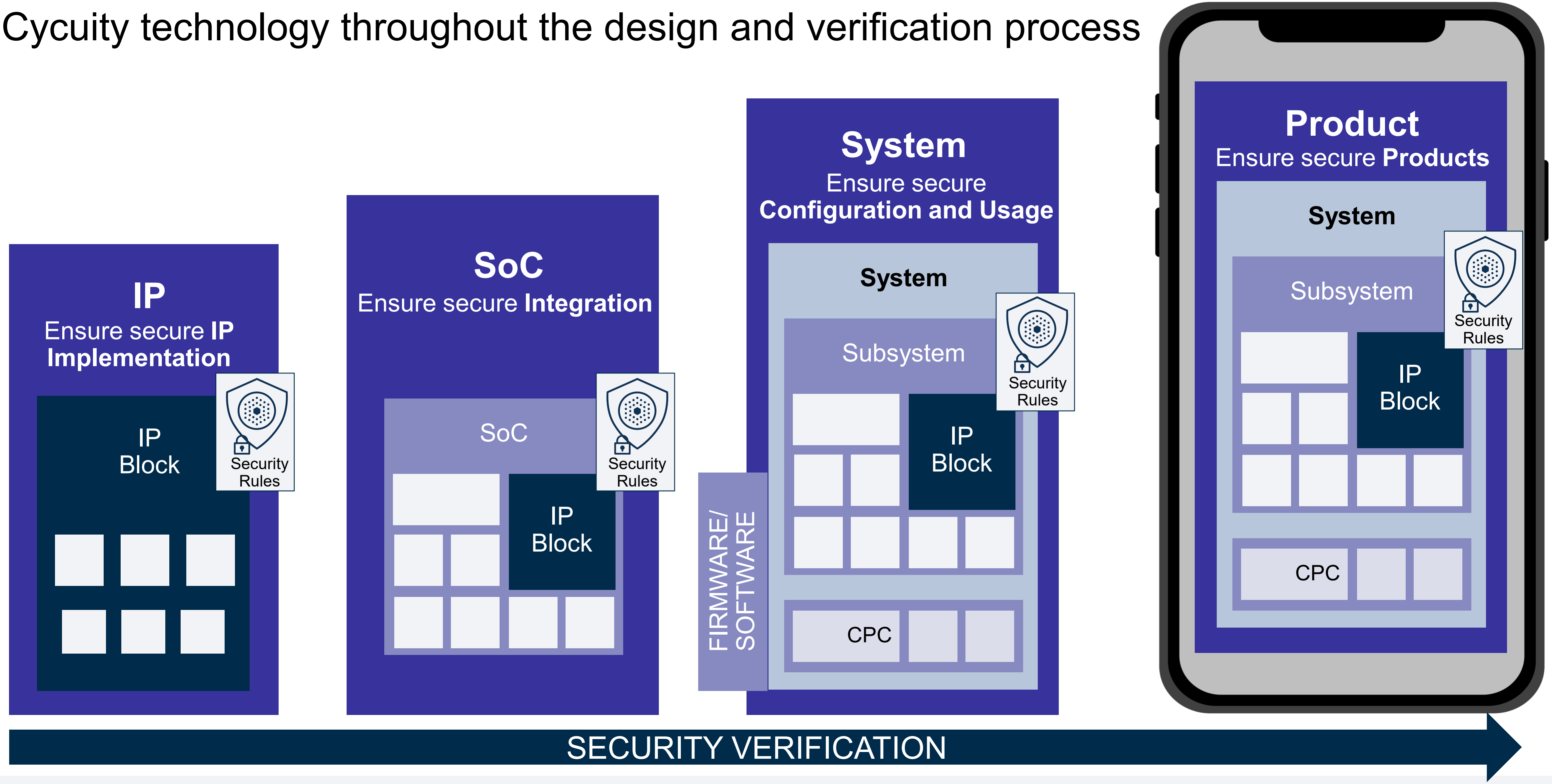
Securing the Semiconductor Design with Arteris

Cycuity technology throughout the design and verification process



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Key Arteris Customer and Ecosystem Highlights from Last Quarter

Arm expanded its partnership with Arteris for Cycuity hardware security assurance technology; already in use on selected Arm CPUs and expanding usage

One of the world's largest hyperscale cloud companies chose to adopt and standardize on Arteris for its infrastructure silicon system IP

One of the top US semiconductor design houses building custom ASICs for various hyperscalers using FlexGen smart NoC IP

Speedata deployed Arteris in its Callisto processor to run large-volume analytics processing

Li Auto has deployed its in-house designed autonomous driving chips in their newest SUV, leveraging Arteris technology

SiEngine has selected Arteris for its next-generation SoC platform for intelligent cockpit, ADAS, and AI cockpit-drive fusion solution

IC-link by IMEC is collaborating with Arteris as part of their ongoing effort to accelerate and simplify development of next-gen HPC chiplets and ASIC chips

Arteris – Converting Historical Trends Into Focused Business Opportunity

Organic product portfolio expansion

- **Jun. 2025 – Chiplet** solution expansion
- **Feb. 2025 – FlexGen** smart NoC IP production
- **Sep. 2024 – Magillem Registers** production
- **Jun. 2023** – **FlexNoC 5** production

Focus on high growth segments & customers

- **Chiplets –** AMD corporate contract
- **AI/ML** – >50% of license $ in 2025 enable AI chiplet and SoC development
- **RISC-V** – Alibaba in 3Q25
- **Automotive** – 1 new & 4 repeat auto OEMs in 2025

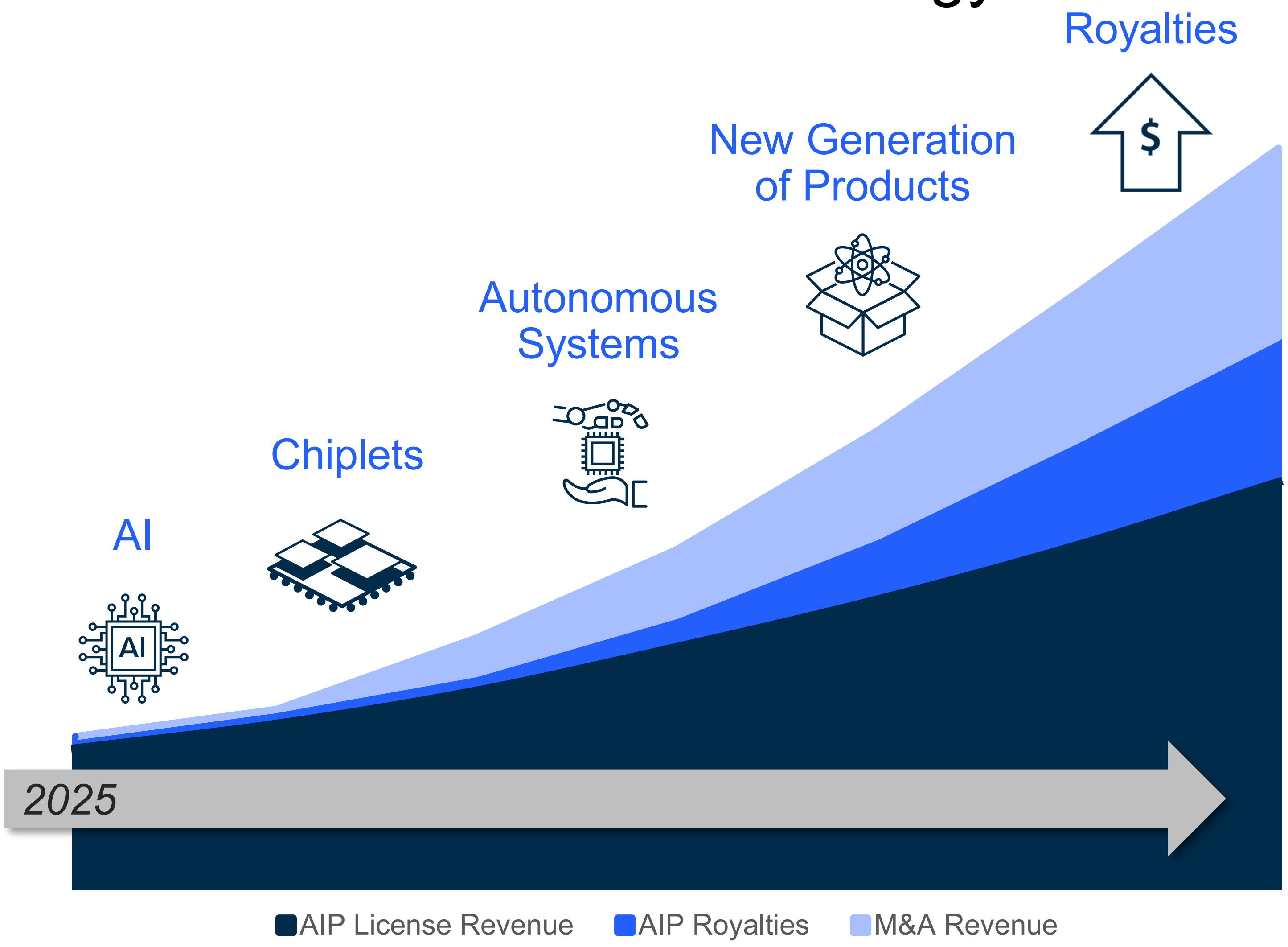
Financial Performance

- Free cash flow positive – 2025
- Strong Balance sheet, no debt
- Customer, geography, application balance for sustainable growth
- **While funding a robust R&D program**

Inorganic growth

- **Jan. 2026** – Cycuity
- **Dec. 2022** – Semifore
- **Nov. 2020** – Magillem

Arteris Revenue Growth Strategy



Management estimates, 2025 - 2029

- Anticipated organic AIP license revenue of high teens/low 20s% CAGR driven by:
  - AI data center, smart edge, physical AI
  - Growth in Multi-Die / Chiplet designs
  - Microcontroller (MCU) support
  - FlexGen Smart NoC IP deployments
  - Semiconductor cybersecurity
  - IP and SW new product releases
- Royalty revenue est. of 30%+ CAGR
- M&A targeted every 1-3 years:
  - Focus on revenue / cash accretive, profitable opportunities
  - Synergies with existing product lines and SoC markets

Financial Overview

Nick Hawkins

2Q 2026 Actuals vs Guidance

In $ millions	2Q 2026 Guidance	2Q 2026 Results	
Revenue	23 – 24	**24.1**	Above top-end of guidance range
ACV + Royalties	95 – 99	**99.5**	Above top-end of guidance range
Non-GAAP Operating Income (Loss)[1]	(3) – (2)	**(4.6)**	Below guidance range[2]
Free Cash Flow[1]	2 – 8	**8.6**	Above top-end of guidance range

1. *Non-GAAP measure: See appendix for reconciliation to closest GAAP measure*
2. *Impacted by higher employer payroll taxes related to French employee RSU vesting*

2Q 2026 Business Highlights

	2Q'25 $M	1Q'26 $M	**2Q'26 $M**	*Y/Y*
Revenue	16.5	22.9	**24.1**	*▲46%*
ACV + Royalties	69.1	92.8	**99.5**	*▲44%*
NG OpEx*	18.6	22.6	**25.5**	*▲37%*
NGOI*	(3.5)	(2.5)	**(4.6)**	*▼29%*
FCF*	(2.8)	(7.4)	**8.6**	*▲$11.4M*
RPO	99.3	118	**135**	*▲36%*

**Non-GAAP measure: See appendix for reconciliation to closest GAAP measure*

- Record-high revenue and ACV+R beating the top end of our 2Q guidance ranges
  - Revenue and ACV+R increased over 40% Y/Y respectively, driven by several large deals in the quarter; new customers as well as expansions of existing business
- Record RPO of $135M, a 36% increase Y/Y
- Non-GAAP operating income (loss) of ($4.6M), which was negatively impacted by unexpected higher employer payroll taxes related to French employee RSUs
- Free cash flow of $8.6M, beating the top end of our guidance range, an $11.4M increase Y/Y, and keeping us on track for another FCF positive FY

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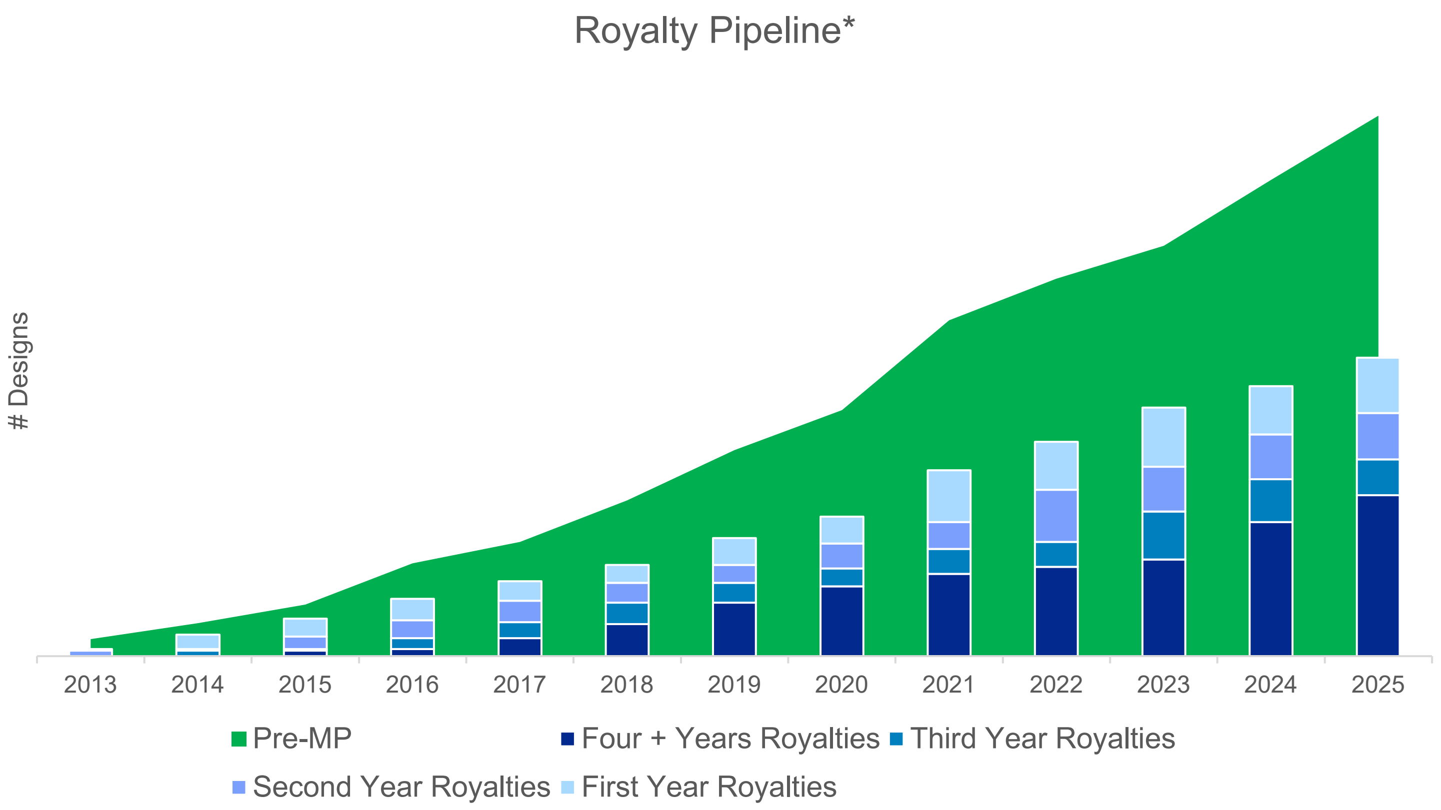
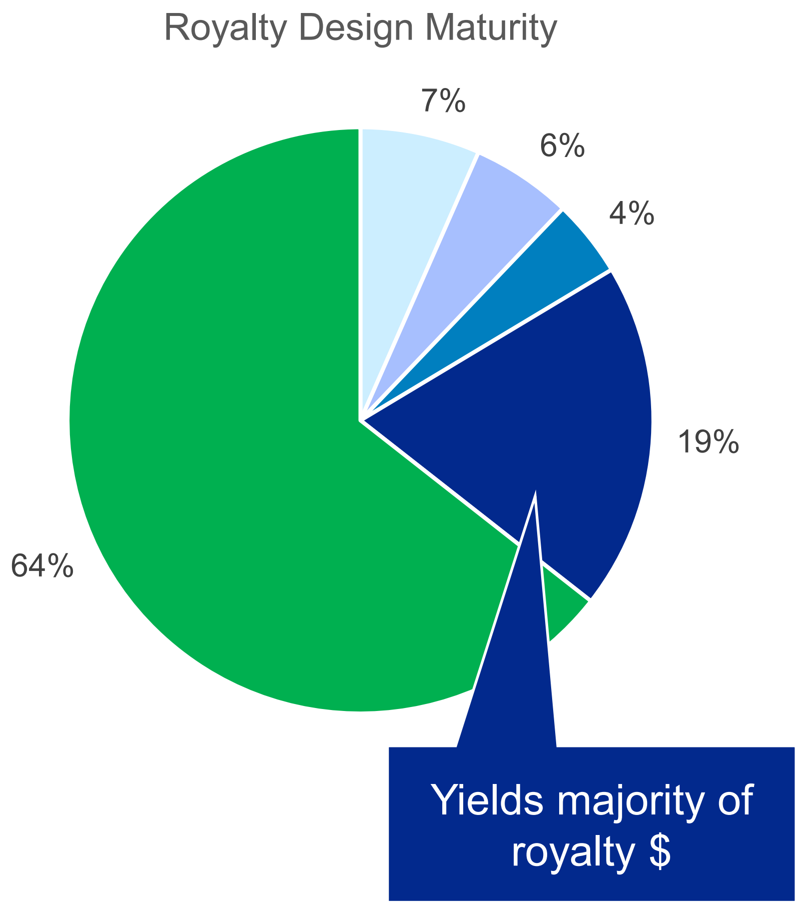
Future Royalty Engine

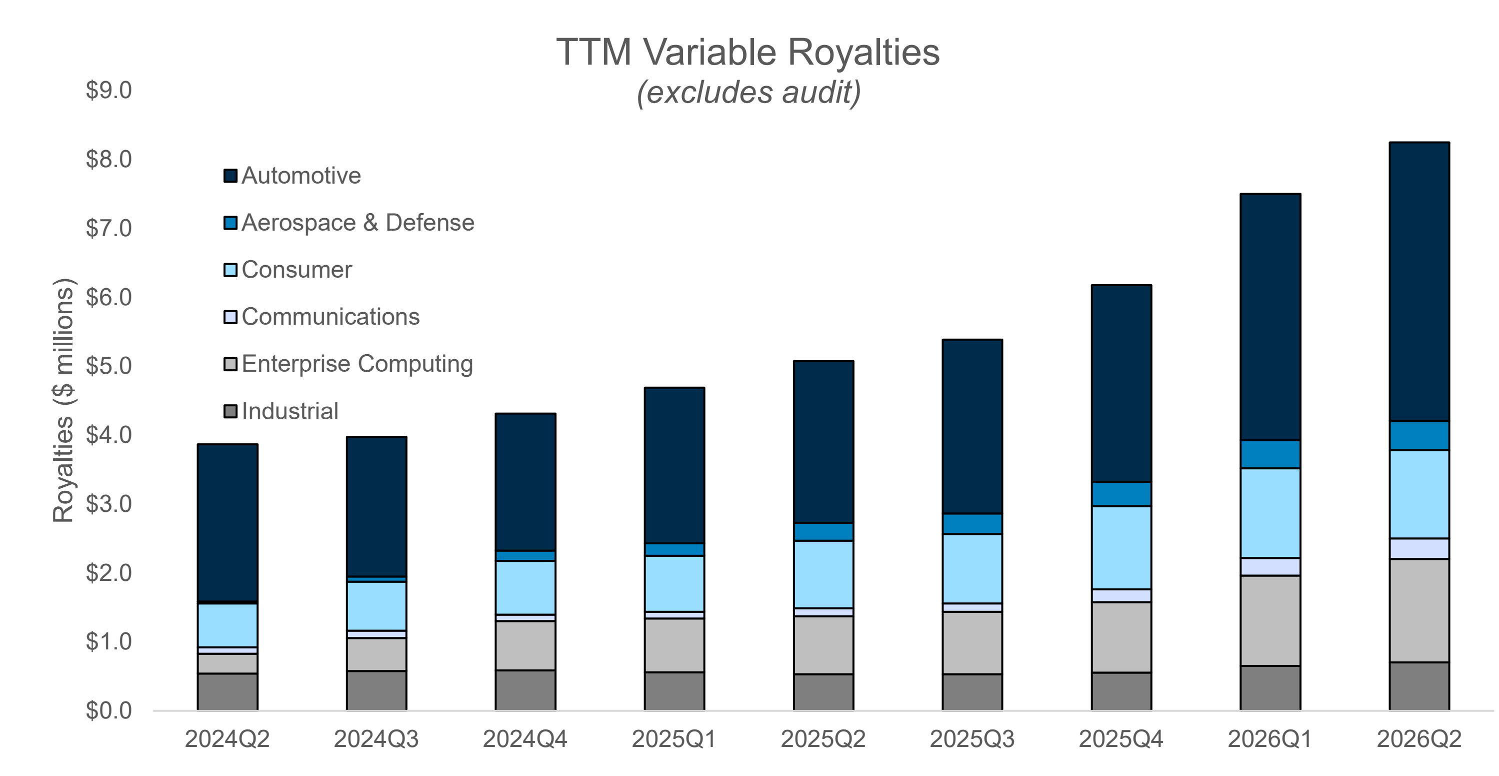
>60% of design wins waiting to deliver royalty revenue; >45% of customer designs in production still ramping up





*Full-year data as of December 31, 2025

Design Wins Result in Future Royalty Expansion



- Primarily driven by automotive, but with growing contributions in all verticals
- New royalty designs reaching mass production every quarter

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Guidance

3Q 2026 Guidance

ACV + Royalties[1]	Revenue[2]
$99M - $103M	$24M - $25M

Non-GAAP Op. Income
($3M) – ($1M)

FY 2026 Guidance

ACV + Royalties[3]	Revenue[4]
$102M - $106M *(maintained)*	$95M - $98M *(+$3.5M from previous guide)*

Non-GAAP Op. Income	Free Cash Flow
($10M) – ($7M) *(-$2M from previous guide)*	$5M – $9M *(maintained)*

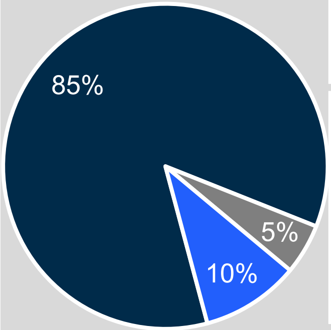
1. *101M at the midpoint, +$26.1M Y/Y (+35%)*
2. *24.5M at the midpoint, +$7.1M Y/Y (+41%)*
3. *104M at the midpoint, +$20.4M Y/Y (+24%)*
4. *96.5M at the midpoint, +$25.9M Y/Y (+37%)*

Arteris has not provided a quantitative reconciliation of its 3Q 2026 or FY 2026 guidance for non-GAAP operating income or free cash flow, in either case, to the closest GAAP measure, within this press release because the Company is unable, without making unreasonable efforts, to calculate certain reconciling items with confidence. These items include, but are not limited to, inventory valuation adjustment and equity securities fair value adjustment. These items, which could materially affect the computation of forward-looking GAAP measures, are inherently uncertain and depend on various factors, some of which are outside of the Company's control.

Key Financial Parameters

Revenue

Derived from 2 Main Sources



License & Support
- Fully ratable (since 2Q'23)
- 2 – 3 year average design term
- Long-term CAGR[1]:high 10s – low 20s%

Royalties
- Long-term CAGR[1]: ~2x license growth rate
- Diverse royalty stream across multiple verticals, dozens of customers

Professional Services
- Includes gov't sub-contracting

Operating Expense

- Ongoing scale up R&D / Channel investment
- Operating leverage from G&A

[1]Management estimates, 2025 - 2029

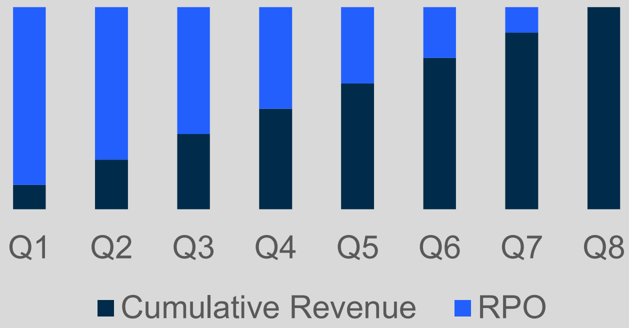
Free Cash Flow

- Majority of deals paid up front
- Revenue deferred
- OpEx as incurred

FCF Leads NGOI

Remaining Performance Obligations (RPO)

Essentially deferred revenue on BS



RPO +36% Y/Y

Illustrative example based on historic performance and not necessarily indicative of future results

Thank you

Appendix - Non-GAAP Financial Measures

To supplement our financial results, which are prepared and presented in accordance with GAAP, we use certain non-GAAP financial measures, as described below, to understand and evaluate our core performance. These non-GAAP measures, which may be different than similarly-titled measures used by other companies, are presented to enhance investors' overall understanding of our financial performance and should not be considered in isolation, as a substitute for, or superior to, the financial information prepared and presented in accordance with GAAP.

We define "Non-GAAP gross profit" and "Non-GAAP gross margin" as GAAP gross profit and GAAP gross margin, respectively, adjusted for stock-based compensation expense included in cost of revenue and amortization of acquired intangible assets included in cost of revenue. We define "Non-GAAP loss from operations" as our GAAP loss from operations adjusted to exclude stock-based compensation expense, amortization of acquired intangible assets and acquisition-related costs, which include advisory, legal, accounting, valuation, other professional or consulting fees, integration costs and changes in the fair value of the contingent consideration related to our acquisition of Cycuity. We define "Non-GAAP net loss" as our net loss adjusted to exclude stock-based compensation, amortization of acquired intangible assets and acquisition-related costs.

The above items are excluded from our Non-GAAP gross profit, Non-GAAP operating expenses and Non-GAAP operating income (loss) because these items are non-cash in nature, or are not indicative of our core operating performance, and render comparisons with prior periods and competitors less meaningful. We believe Non-GAAP gross profit, Non-GAAP operating expenses and Non-GAAP operating income (loss) provide useful supplemental information to investors and others in understanding and evaluating our results of operations, as well as provide a useful measure for period-to-period comparisons of our business performance.

We define free cash flow as net cash provided by (used in) operating activities less cash used for purchases of property and equipment. We believe that free cash flow is a useful indicator of liquidity that provides information to management and investors, even if negative, about the amount of cash used in our operations other than that used for investments in property and equipment.

Appendix - GAAP To Non-GAAP Reconciliation

Operating expenses

In $ thousands	Three Months Ending: Jun 30, 2025	Mar 31, 2026	Jun 30, 2026
Operating expenses	23,008	28,986	34,435
Less:			
Stock-based compensation expense	4,265	5,190	5,963
Amortization of acquired intangible assets	167	636	727
Acquisition-related costs	-	584	2,215
Non-GAAP operating expenses	18,576	22,576	25,530

Appendix - GAAP To Non-GAAP Reconciliation

Operating income (loss)

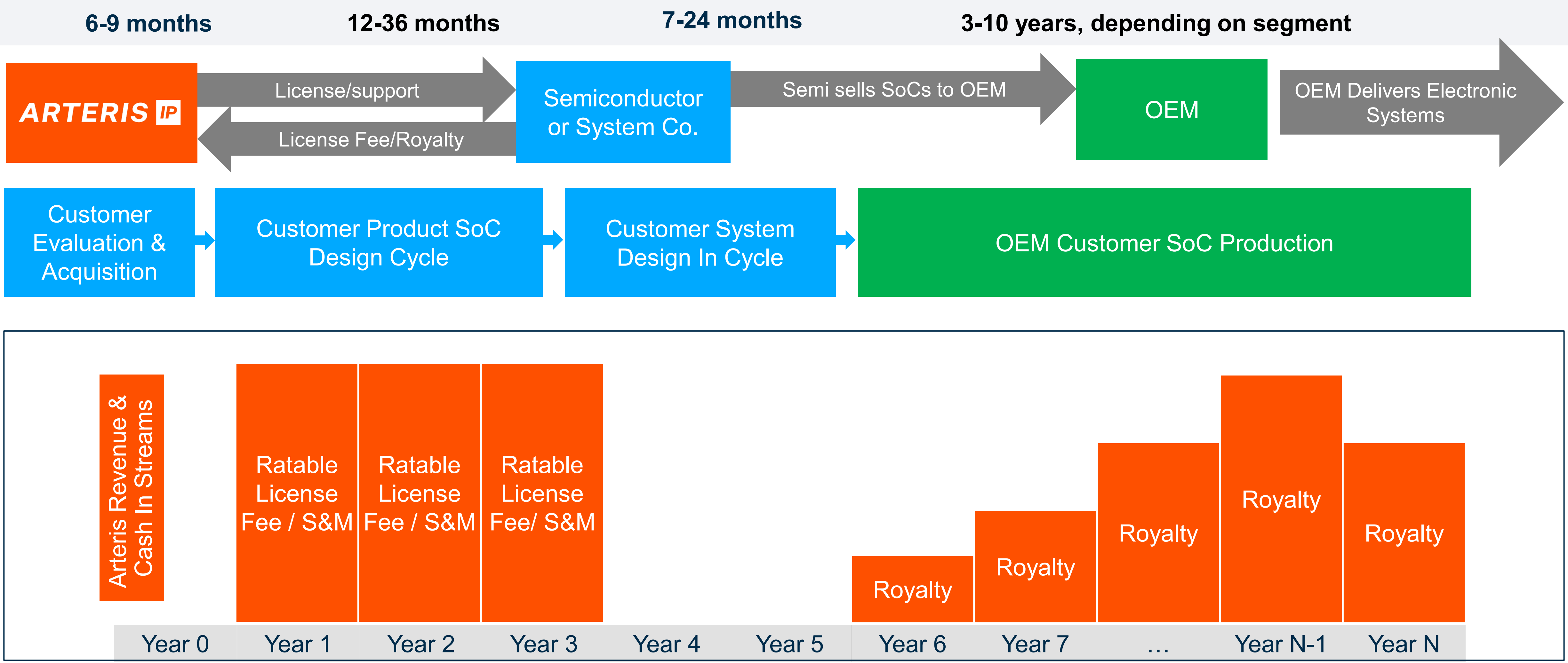
In $ thousands	Three Months Ending: Jun 30, 2025	Mar 31, 2026	Jun 30, 2026
Operating Income (Loss)	(8,248)	(9,300)	(13,908)
Add:			
Stock-based compensation expense	4,497	5,511	6,347
Amortization of acquired intangible assets	217	686	777
Acquisition-related costs	-	584	2,215
Non-GAAP Operating Income (Loss)	(3,534)	(2,519)	(4,569)

Appendix –Free Cash Flow

	Three Months Ending:		
In $ thousands	**Jun 30, 2025**	**Mar 31, 2026**	**Jun 30, 2026**
Net cash provided by (used in) operating activities	(2,485)	(7,062)	9,210
Less:			
Purchase of property and equipment	355	295	595
Free cash flow	(2,840)	(7,357)	8,615

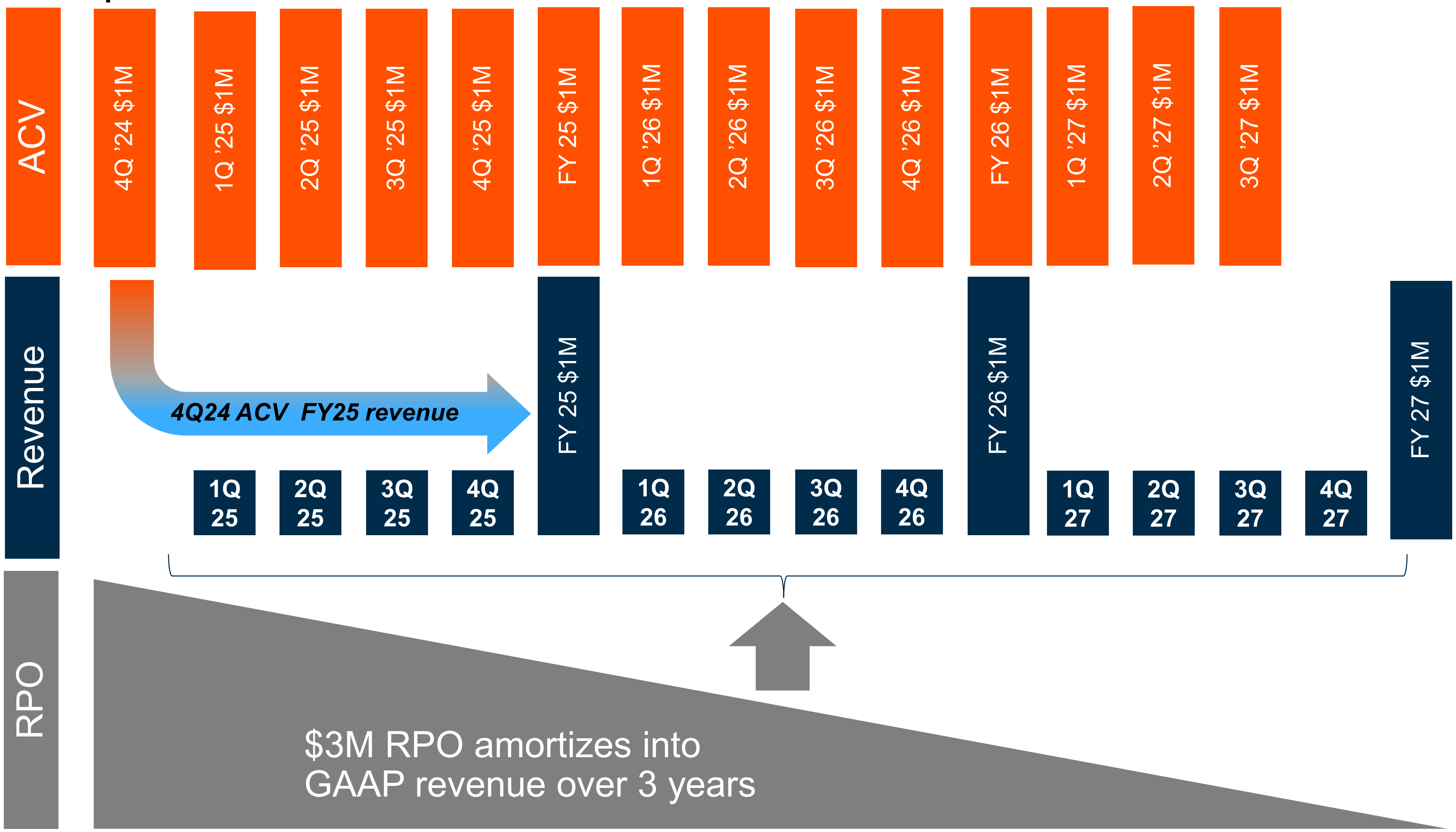
Appendix - Arteris Business Model

Arteris customers have delivered 4 Billion+ chips/chiplets based on 975+ SoC projects



Appendix - License Revenue / ACV / RPO Model

Example $3M deal, 36-month license term



$3M / 3 Yr
Deal Signed
12/29/24

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