Accelerating The Creation Of Semiconductors

Corporate Overview

Charlie Janac
CEO

Nick Hawkins
CFO

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ARTERIS IP

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This presentation and the accompanying oral presentation also contain estimates and other statistical data made by independent parties and by us relating to market size and growth and other data about our industry. This data involves a number of assumptions and limitations, and you are cautioned not to give undue weight to such estimates. In addition, projections, assumptions, and estimates of our future performance and the future performance of the markets in which we compete are necessarily subject to a high degree of uncertainty and risk.

In addition to the financials presented in accordance with U.S. generally accepted accounting principles ("GAAP"), this presentation includes the following non-GAAP metrics: non-GAAP operating expenses, non-GAAP operating income (loss) and free cash flow. Non-GAAP metrics have limitations as analytical tools and you should not consider them in isolation or as a substitute for or superior to the most directly comparable financial measures prepared in accordance with U.S. GAAP. There are a number of limitations related to the use of non-GAAP metrics versus their nearest GAAP equivalents. Other companies, including companies in our industry, may calculate non-GAAP metrics differently or may use other measures to evaluate their performance, all of which could reduce the usefulness of our non-GAAP metrics as tools for comparison. We urge you to review the reconciliation Arteris' non-GAAP metrics to the most directly comparable GAAP financial measures, and not to rely on any single financial measure to evaluate our business. See the Appendix for reconciliation between each non-GAAP metric and the most comparable GAAP measure.

Arteris is unable to provide a reconciliation of certain non-GAAP guidance metrics in this presentation because the corresponding GAAP measures are not accessible on a forward-looking basis. Due to the potential variability and limited visibility of the excluded items, providing such reconciliation would necessitate unreasonable effort.

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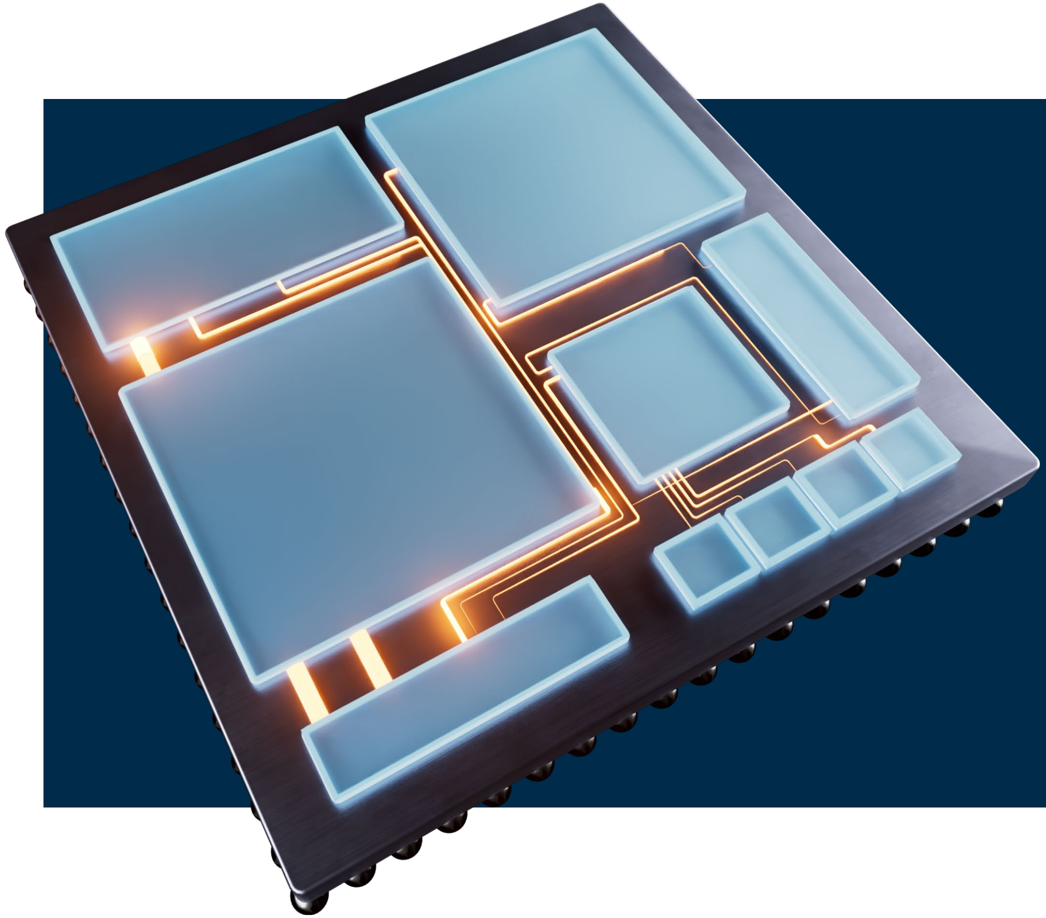
Semiconductors Process Data, Store Data and Move Data

Optimizing data transport critical to function, performance & power efficiency



System IP (NoCs) → data movement

Processors → data processing/compute

Logic → peripheral compute

Memory → data storage

Arteris Improves Data Movement in SoCs & Between Chiplets

From Smart Electronics to the Most Complex Autonomous Systems

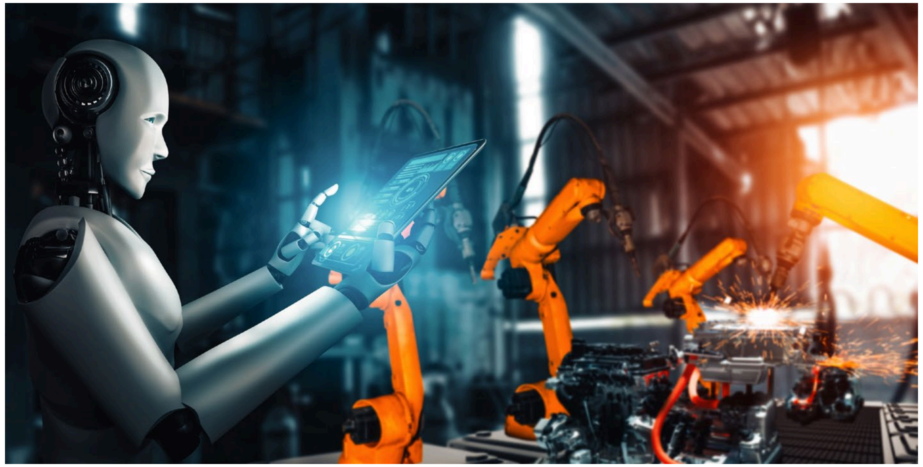
Arteris Product Lines

Network-on-Chip IP

- Cache coherent NoC IP
- Non-coherent NoC IP
- Data movement options

SoC Integration Software

- SoC system level connectivity
- Hardware-software integration
- IP block packaging



Expansion of AI workloads increasing chip complexity and the value of efficiently moving data across chiplets & SoCs

- Multi-billion $ market
- With 10-15% growth

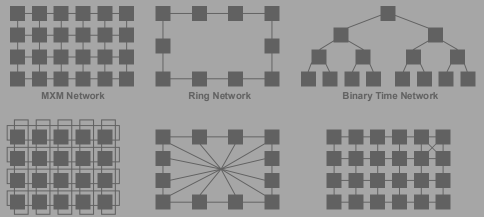
Expanding Arteris' business opportunity for commercial system IP

Arteris Staying Ahead of Electronic System Innovation

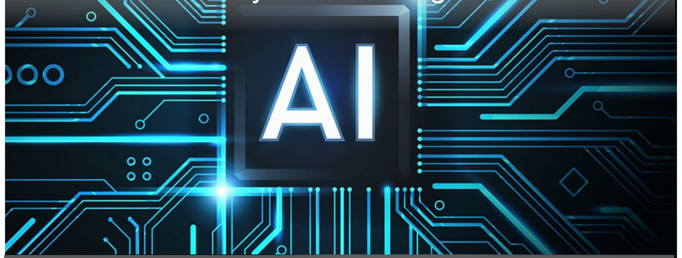
Dramatic expansion of AI workloads - Arteris provides sophisticated solutions

Adoption of New Semi. Technologies

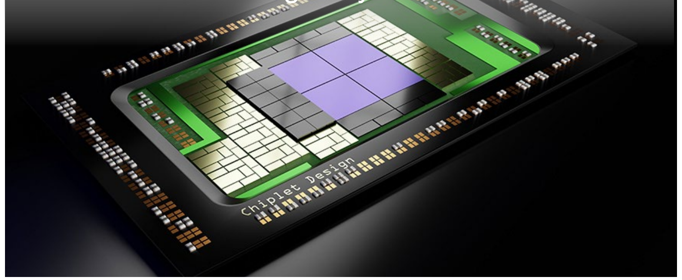
More Processors & IP Blocks
– data movement key competency –



Artificial Intelligence
– electronic systems making decisions –



Chiplets
– multi-die SoCs go beyond Moore's Law –



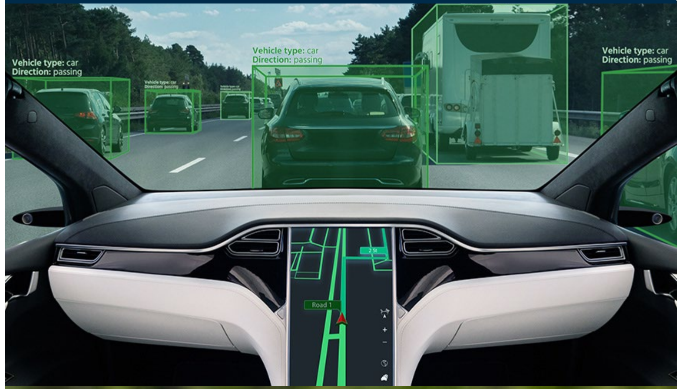
Smart Systems Connected Everywhere

Connections to the internet and each other

Regionalization of Semiconductor Industry

Government strategies, investments & subsidies

Supporting Silicon Based Business Creation



ARTERIS IP

Arteris – The Leading System IP Company and NoC Pioneer

Global customer base producing billions of SoCs

System IP Leader

- Pioneers of Network-on-Chip (NoC) IP
- October 2021 IPO (Nasdaq: AIP)
- Global Team of 300+ employees
- 113 patents issued, 142 applications[1]
- ISO 9001:2015 and ISO 26262 Certified

DEKRA Quality Management ISO 9001:2015 certified · TÜV SÜD ISO 9001 · exida

Proven Customer Successes

- 3.9B+ SoCs shipped in electronic systems
- 900+ SoC design starts[1]
- Used by 9 out of the top 10 semiconductor companies
- 90%+ customer retention rate[2]
- Ecosystem: Any processor, any IP, any EDA, and any Foundry

Connected with the Ecosystem

arm · SYNOPSYS · SiFive · cadence · ANDES · SIEMENS · MIPS · Semidynamics · XUANTIE · Fraunhofer · tsmc · intel foundry · SAMSUNG Foundry · GlobalFoundries · IEEE · accellera · IP-XACT · SystemRDL · RISC-V · UVM · UPF · UCIe Universal Chiplet Interconnect Express · CXL Compute Express Link

Global R&D and Customer Support

[1] As of September 30,2025

[2] Over a three year period ending September 30,2025

Diversified Customer Base

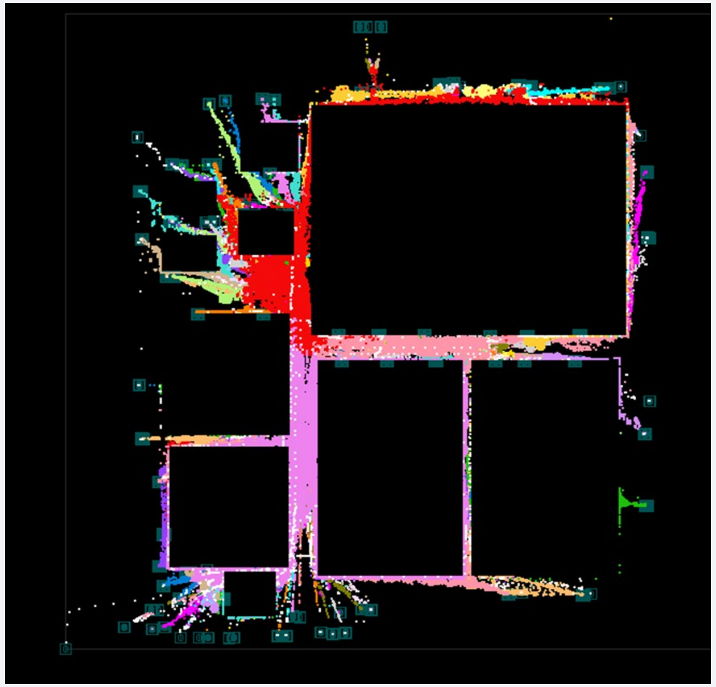
Subset of Publicly Disclosed Customers

AMD · SAMSUNG · RENESAS · NXP · altera · Microchip · socionext · BOSCH · NIO · mobileye · Texas Instruments · MaxLinear · TOSHIBA · HONDA The Power of Dreams · ST life.augmented · SiMa.ai · tenstorrent · HAILO · HYUNDAI MOBIS · ESWIN · Baidu 百度 · rebellions · Enflame · KYOCERA · Horizon Robotics · NEUREALITY · synaptics · TIER IV · indie · INUITIVE · AION SILICON · alchip · VeriSilicon · WHALECHIP · leidos · Telechips · DREAM CHIP · Corerain · SCALINX · DEEPX · SAPEON · GUC · supergate · ULTICHIP · Achronix · PLiOPS · arbe · canaan · SemiDrive · RapidSilicon · Hanwha Vision · GigaDevice · JLQ · AutoChips · siengine · UHNDER · FURIOSA · 2V SYSTEMS · AXELERA · EDGEQ · bos · eyenix · Blu Wireless · mobilint · CALTERAH · SPEEDATA · AXIADO · TENSORDYNE · neubla · Fullhan · nextchip · Rockchip · asicland · MORNINGCORE · pixelworks · ANALOG INFERENCE · GCT · HOUMO.AI · TSING MICRO · BlueOceanSmart · BLACK SESAME · UNITED MICRO · MegaChips · UNISOC · octasic · Actions · auradine · MONTAGE Technology · vayyar · Hisense · V-SILICON · VITEC · METAX · SEQUANS

ARTERIS IP

Continuous Delivery of System IP Technology
FlexGen smart NoC IP momentum since Q1`25 introduction

Challenge: SoC design complexity has surpassed manual human capabilities, requiring smart NoC automation. Modern SoCs have 5 to 20+ unique NoC instances and each instance can require 5-10 iterations.



Solution: Automated NoC IP generation based on advanced algorithms, Incremental features and manual editing capability.

Benefits: to 10x better NoC IP productivity, to 30% shorter wire length, to 5% lower overall latency, lower power & competitive area vs manual NoCs.

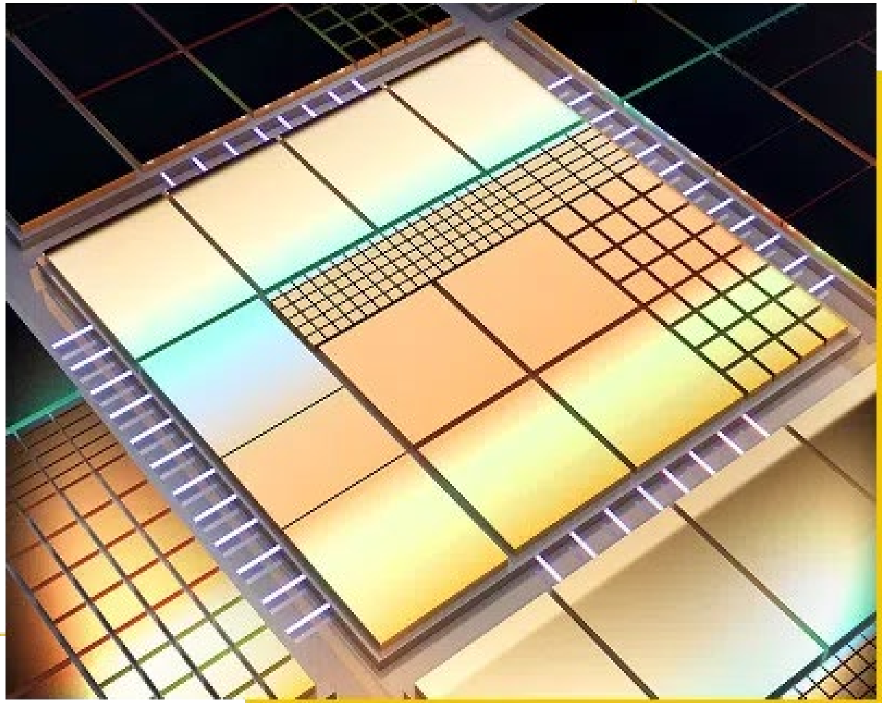
Accelerating Momentum:

- **6 licensed customers, 25+ evaluations underway**
- **Multi-unit FlexGen orders from AMD and Altera**
- **Multiple silicon projects underway**

Arteris Expanding Multi-Die Solution to Meet Demand for Chiplets

- **Challenge:** With the rise of AI, computational power requirements are exceeding what is possible through traditional monolithic die designs.
- **Solution:** Arteris Multi-Die Solution delivers foundational technology for rapid chiplet-based innovation across all markets.
- **Ecosystem:** Collaboration for Multi-Die Physical Integration and Interoperability

arm AMBA · UCIe Universal Chiplet Interconnect Express · SYNOPSYS® · cadence



More Than 50% of Arteris Business is for AI Applications in 2025

Data movement is a key competency of AI SoCs → Arteris offers superior bandwidth & features

	Number of Customers – Sep 2025	Arteris Customers → 270 AI SoC design wins
Data Center Training & Inference	30+	Major US Hyperscaler #1, Major US Hyperscaler #2, Major US Hyperscaler #3, Hyperscaler #4, Hyperscaler #5, Major US Automotive EV OEM #1, 蓝洋智能 BlueOceanSmart, Cambricon 寒武纪科技, Corerain 鲲云科技, Enflame 燧原科技, Achronix Data Acceleration, AMD, Bai du 百度, BITMAIN, DEEPX, ESWIN, INNOSILICON, Lynxi 灵汐科技, METAX 沐曦集成电路, MONTAGE Technology, neubla, rebellions_, TENSORDYNE, tenstorrent, NEUREALITY, 清微智能 TSING MICRO, 瀚博半导体 Vastai Technologies, VeriSilicon
Edge Inference	60+	ANALOG INFERENCE, AXELERA ARTIFICIAL INTELLIGENCE, bos SEMICONDUCTORS, Canaan, EDGEQ, Horizon Robotics, INUITIVE, mobilint, Movidius an Intel company, 眸芯科技(上海)有限公司 Molchip Technology (Shanghai) Co.,Ltd., TENSORDYNE, RENESAS, SOPHGO 算能, alchip, BOSCH, BLACK SESAME TECHNOLOGIES, DREAM CHIP, FURIOSA, HAILO, 后摩智能 HOUMO.AI, 天数智芯 Iluvatar CoreX, mobileye, nextchip, NXP, siengine 芯擎科技 Technology, SAMSUNG, 芯驰 SemiDrive, socionext, ST life.augmented, Telechips, SiMa.ai, BMW, Toshiba, Automotive EV OEM #1, Automotive EV OEM #2, Automotive EV OEM #3, Automotive EV OEM #4, Automotive EV OEM #5, Automotive EV OEM #6, Automotive EV OEM #7, Automotive EV OEM #8, Automotive EV OEM #8, Major Robotaxi #1, Major Robotaxi #2

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Key Arteris Customer and Ecosystem Highlights from Last Quarter

Altera expanded its broad use of existing Arteris IP products with Ncore, FlexGen, and licensed Magillem integration automation software to advance intelligent computing across AI cloud to edge applications

AMD expanded their Arteris footprint, beyond the Q2 FlexGen order announcement, with additional licenses of Arteris NoC IP and Software products in the third quarter, to be used for a broad range of applications

Alibaba DAMO Academy expanded collaboration enabling their RISC-V CPU cores to better integrated Arteris IPs to help enable mutual customers for edge AI, server, communications, and automotive applications

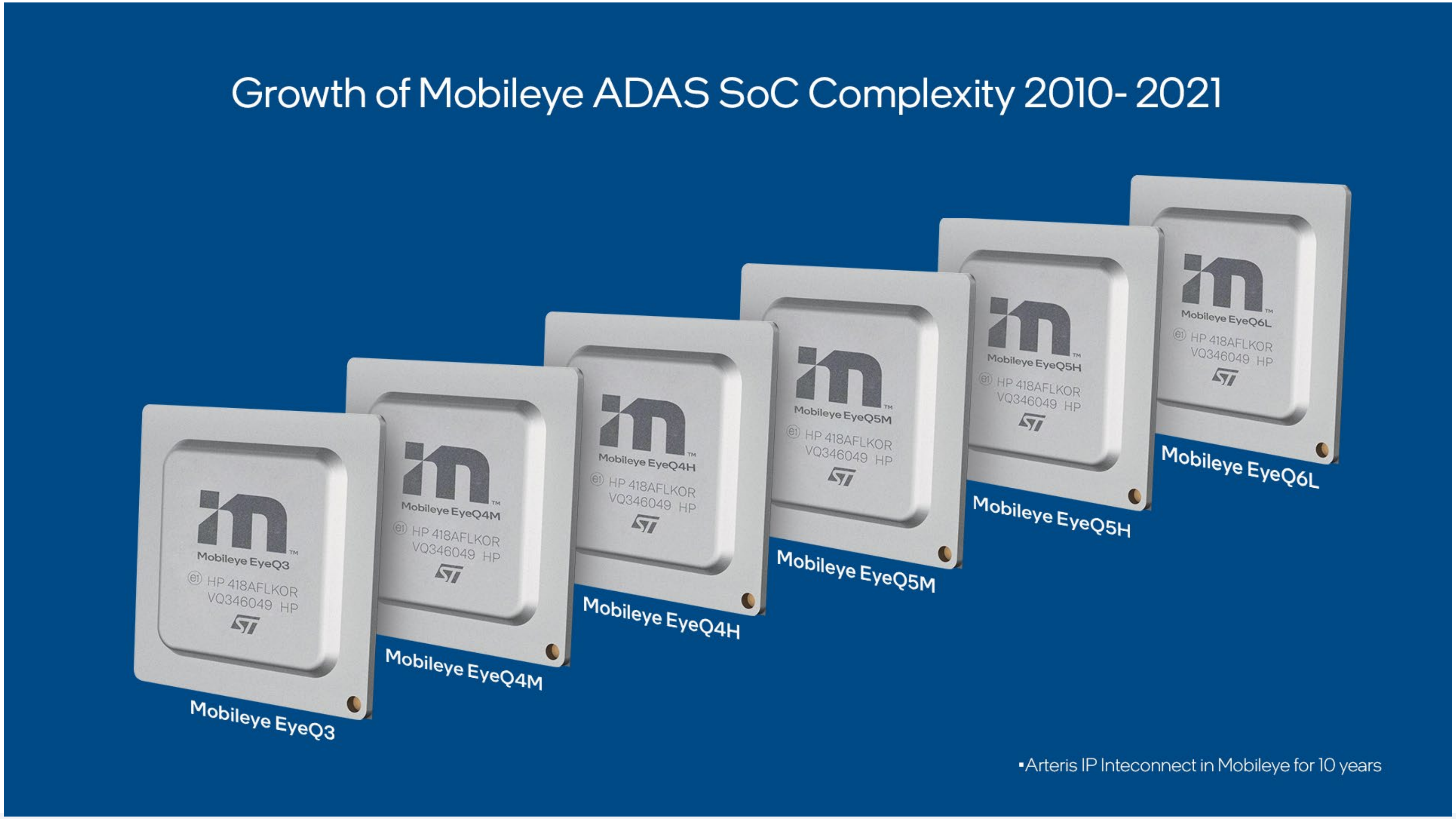
NanoXplore, a provider of silicon technology to address the needs of the aerospace, defense, avionics, and industrial markets licensed FlexGen smart NoC IP to be used in space

2V Systems licensed Ncore and FlexNoC interconnect for development of an IO Hub chiplet to meet the bandwidth, latency, and efficiency demands of data center computing and cloud infrastructure

Arteris joined the Ultra Accelerator Link Consortium, or UALink, where the goal is to establish an optimized scale-up ecosystem across multiple AI accelerators, with Arteris' NoC IPs serving a key role in AI chiplets, SoCs

12 Year Relationship Between Mobileye and Arteris

Meeting stringent requirement of automotive ADAS SoCs



- Reasons for Adoption
  - Superior NoC Performance
  - Superior NoC
  - Flexibility
  - Low NoC power consumption
  - Unique cache coherency features
  - Attentive support
  - Automotive feature focus
  - Proven record of delivery
  - Architectural Flexibility

Strategies for Arteris' Multi-year Growth

Organic product portfolio expansion

- **Jun. 2025 – Chiplet** solution expansion
- **Feb. 2025 – FlexGen** smart NoC IP production
- **Sep. 2024 – Magillem Registers** production
- **Jun. 2023** – **FlexNoC 5** production

Focus on high growth segments & customers

- **Chiplets –** AMD corporate contract
- **AI/ML** – >50% of license $ in 2025 enable AI chiplet and SoC development
- **RISC-V** – Alibaba in 3Q25
- **Automotive** – 1 new & 4 repeat auto OEM in 2025

Financial Performance

- Free cash flow forecast positive – 2025
- Strong Balance sheet, no debt
- Customer, geography, application balance for sustainable growth
- Funding a robust R&D program

Inorganic growth

- **Jan. 2026** – Acquisition of Cycuity
- **Dec. 2022** – Acquisition of Semifore
- **Nov. 2020** – Acquisition of Magillem

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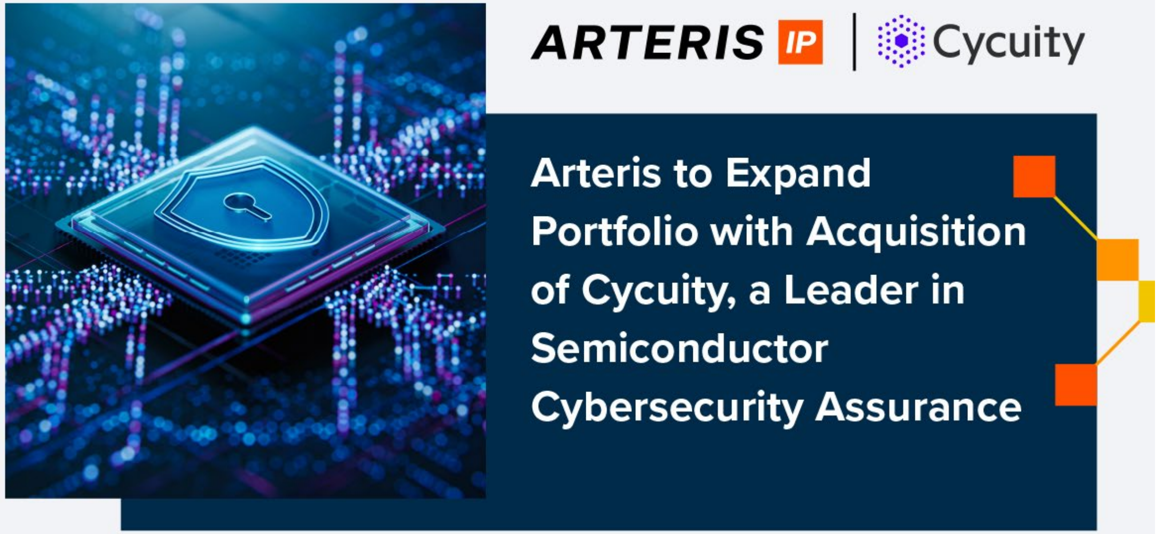
Arteris Acquires Cycuity

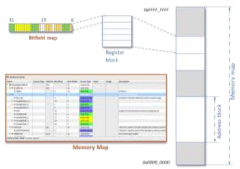
- Cycuity's technology and expertise strengthens Arteris' product portfolio, enabling chip designers to understand and improve data movement security in chiplets and SoCs.
- Silicon vulnerabilities can result in compromised systems exposing unprotected information, a trend accelerated by the proliferation of AI and chiplets.
- Semiconductor cybersecurity assurance is becoming critical to all types of chip designs, as the threat landscape has expanded to the hardware layer.



From AI data centers to edge devices, designs now require a trustworthy silicon foundation to ensure security for all electronic systems. Arteris' products provide the backbone to move the data across SoCs and chiplets, making them naturally complementary to hardware security assurance. Together we can accelerate the secure design and deployment of microelectronics for commercial and defense engineering teams.

- Andreas Kuehlmann, CEO of Cycuity

Arteris' Roadmap → Technology, Support and Eco-system

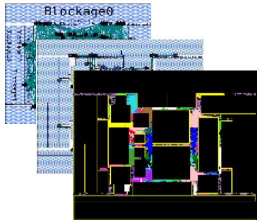


Magillem Registers

Next generation of of HW/SW Interface

FlexGen

Smart NoC IP for non-coherent networks



IP-XACT

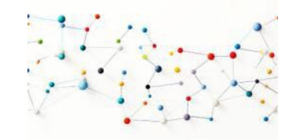
Magillem Packaging

3rd party and In-house IP Packaging

Coherent Multi-Die

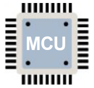
Expanded support for Die-to-Die Cache Coherent Chiplets

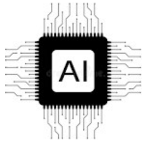
IP-XACT

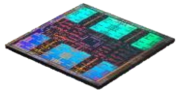


Magillem Connectivity

Next generation of IP/SoC assembly w/ IEEE 1685-2022







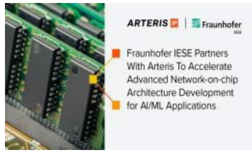
Q4\`24 | Q1\`25 | Q2\`25 | Q3\`25 | Q4\`25 | Q1\`26-Q2'26

arm

Automotive Partnership

SiFive

Edge AI Partnership



Fraunhofer IESE

DRAM-NoC Partnership

ANDES TECHNOLOGY

RISC-V Ecosystem

SYNOPSYS®

RISC-V Ecosystem

MIPS

RISC-V Ecosystem

- IP: Expanded Arm & RISC-V support
- EDA: Better flow support for NoCs
- Foundries: New advanced nodes

ARTERIS IP

Financial Overview

Nick Hawkins

3Q 2025 Actuals vs Guidance

In $ millions	3Q 2025 Guidance	3Q 2025 Results	
Revenue	16.8 – 17.2	**17.4**	Above top-end of guidance range
ACV + Royalties	69.5 – 72.5	**74.9**	Above top-end of guidance range
Non-GAAP Operating Income (Loss)*	(4.0) – (3.0)	**(3.5)**	At mid-point of guidance range
Free Cash Flow*	0.5 – 3.5	**2.5**	Above mid-point of guidance range

**Non-GAAP measure: See appendix for reconciliation to closest GAAP measure*

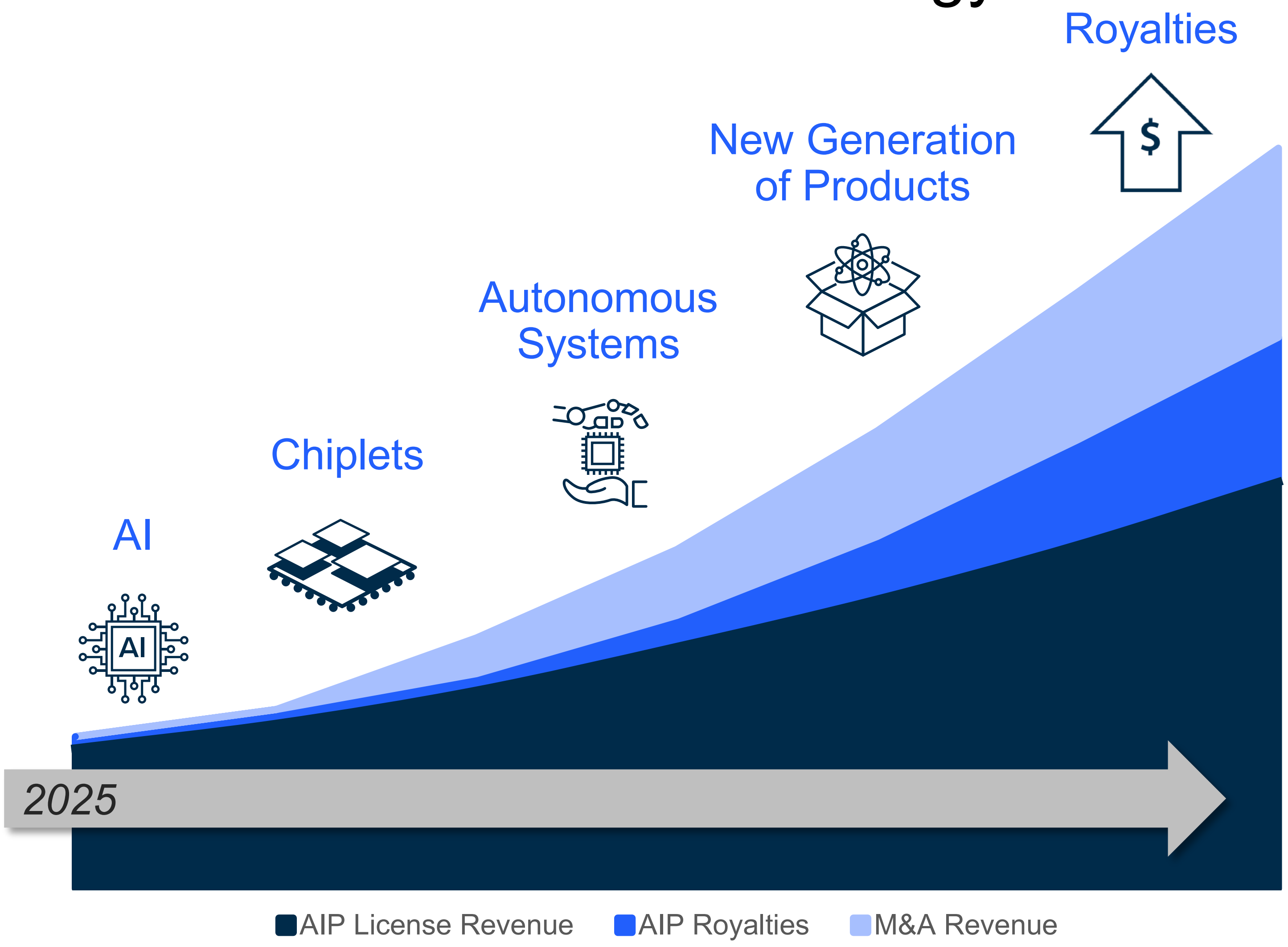
3Q 2025 Business Highlights

	3Q'24	2Q'25	**3Q'25**	*Y/Y*
Revenue	14.7	16.5	**17.4**	*▲18%*
ACV + Royalties	60.5	69.1	**74.9**	*▲24%*
NG OpEx*	16.8	18.6	**19.5**	*▲16%*
NGOI*	(3.3)	(3.5)	**(3.5)**	▼6%
FCF*	1.1	(2.8)	**2.5**	*▲$1.4M*
RPO	78.4	99.3	**104.7**	*▲34%*

**Non-GAAP measure: See appendix for reconciliation to closest GAAP measure*

- Record high RPO in the quarter exceeding $100M for the first time, +34% Y/Y
- Revenue and ACV+R exceeded the top-end of our guidance ranges, at $17.4M and $74.9M, respectively, increases of 18% and 24% Y/Y, respectively
- Non-GAAP operating expense was $19.5M, up 16% Y/Y due to weakening USD increasing the cost of overseas operations, and increased investments in R&D and field application engineering teams
- Non-GAAP operating income (loss) was flat Q/Q and down 6% Y/Y at ($3.5M)
- Free cash flow positive in the quarter at $2.5M, and positive $2.3M YTD

Arteris Revenue Growth Strategy



Management estimates, 2025 - 2029

- Anticipated organic AIP license revenue of high teens/low 20s % CAGR driven by:
  - AI from data center to smart edge
  - Growth in Multi-Die / Chiplet designs
  - Microcontroller (MCU) support
  - FlexGen Smart NoC IP deployments
  - IP and SW new product releases
- Royalty revenue est. of 30+ % CAGR
- M&A targeted every 1-3 years:
  - Focus on revenue / cash accretive, profitable opportunities
  - Synergies with existing product lines and SoC markets

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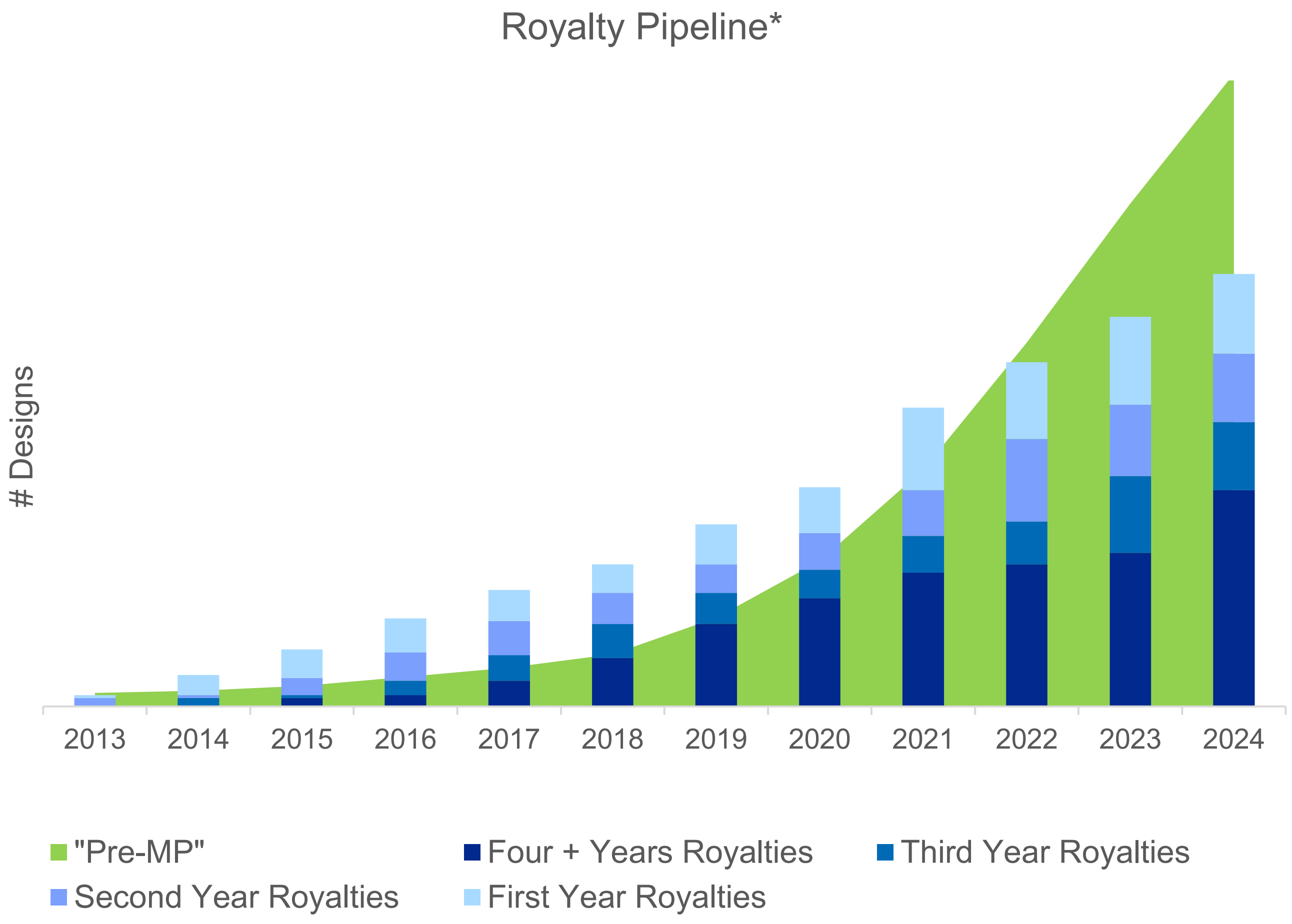
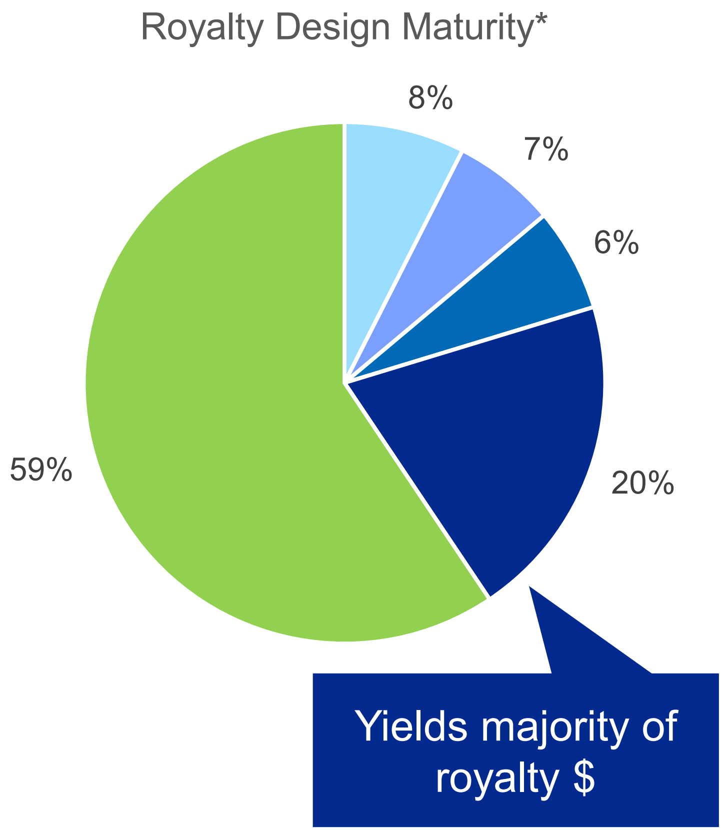
Future Royalty Engine

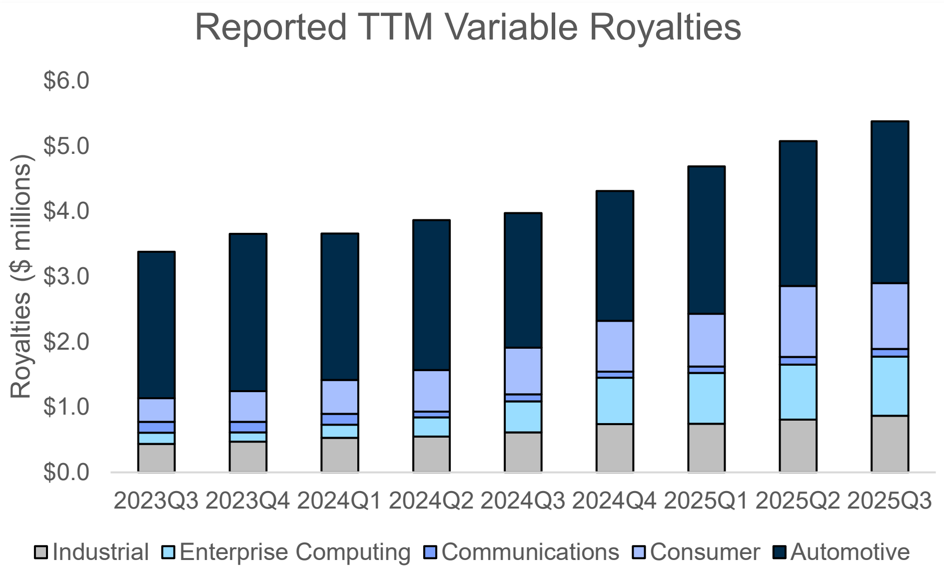
~60% of design wins waiting to deliver royalty revenue; >50% of customer designs in production still ramping up

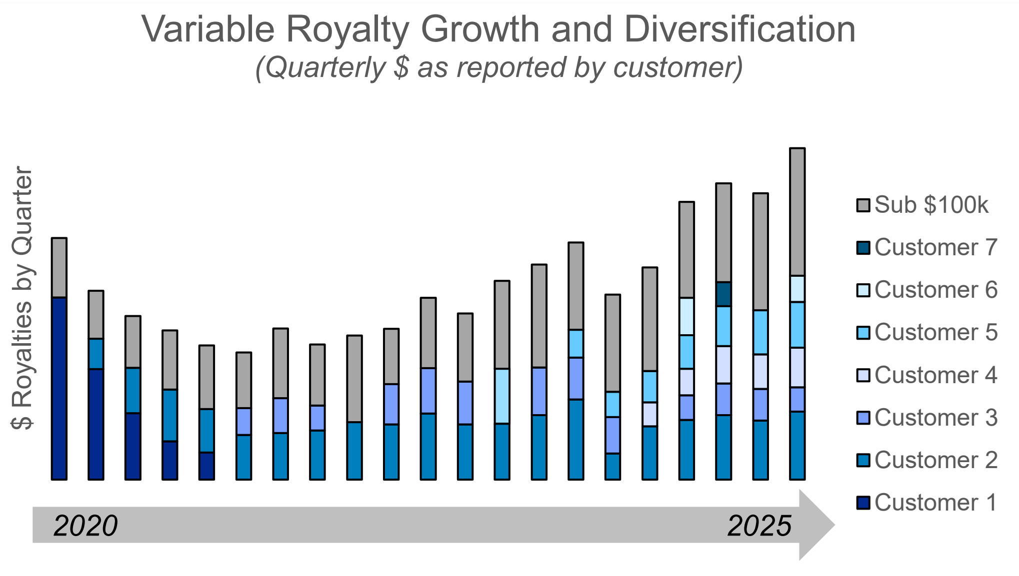




*Full-year data as of December 31, 2024

Design Wins Result in Future Royalty Expansion



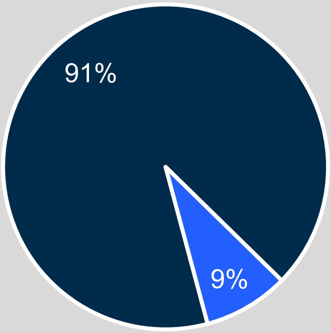


- Primarily driven by automotive, but with growing contributions by consumer and enterprise

- Increasingly diverse royalty customer base, with 4-5 customers reporting $100k+ quarterly reports, vs 2020 with 1-2 significant customers, dominated by HiSilicon

Key Financial Parameters

Revenue
Derived from 2 Main Sources



License & Support
- Fully ratable (since 2Q’23)
- 2 – 3 year average design term
- Long-term CAGR[1]:high teens – low 20s%

Royalties
- Long-term CAGR[1]: ~2x license growth rate
- Diverse royalty stream across multiple verticals, dozens of customers

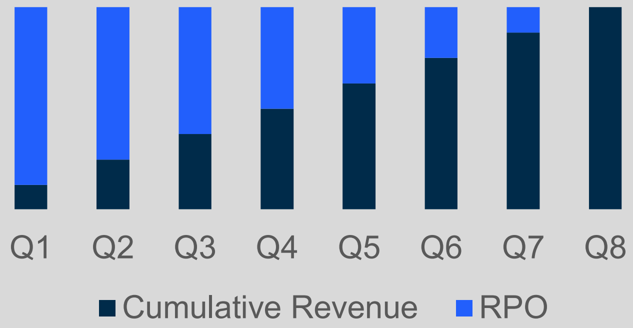
Operating Expense
- Ongoing scale up R&D / Channel investment
- Operating leverage from G&A

[1]Management estimates, 2024 - 2029

Free Cash Flow
- Majority of deals paid up front
- Revenue deferred
- OpEx as incurred

FCF Leads NGOI

Remaining Performance Obligations (RPO)
Essentially deferred revenue on BS



RPO +34% Y/Y

Illustrative example based on historic performance and not necessarily indicative of future results

Guidance

4Q 2025 Guidance

ACV + Royalties[1]	Revenue[2]
$74M - $78M	$18.4M - $18.8M

Non-GAAP Op. Income	Free Cash Flow
($3.3M) – ($2.3M)	$0.2M – $3.2M

FY 2025 Guidance

ACV + Royalties[3]	Revenue[4]
$74M - $78M	$68.8M - $69.2M

Non-GAAP Op. Income	Free Cash Flow
($13.5M) – ($12.5M)	$2.5M – $5.5M

1. *76M at the midpoint, +$10.9M Y/Y (+17%)*
2. *18.6M at the midpoint, +$3.1M Y/Y (+20%)*
3. *76M at the midpoint, +$10.9M Y/Y (+17%)*
4. *69M at the midpoint, +$11.3M Y/Y (+20%)*

Arteris has not provided a quantitative reconciliation of its 3Q 2025 or FY 2025 guidance for non-GAAP operating income or free cash flow, in either case, to the closest GAAP measure, within this press release because the Company is unable, without making unreasonable efforts, to calculate certain reconciling items with confidence. These items include, but are not limited to, inventory valuation adjustment and equity securities fair value adjustment. These items, which could materially affect the computation of forward-looking GAAP measures, are inherently uncertain and depend on various factors, some of which are outside of the Company's control.

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Thank you

Appendix - Non-GAAP Financial Measures

To supplement our financial results, which are prepared and presented in accordance with GAAP, we use certain non-GAAP financial measures, as described below, to understand and evaluate our core performance. These non-GAAP measures, which may be different than similarly-titled measures used by other companies, are presented to enhance investors' overall understanding of our financial performance and should not be considered in isolation, as a substitute for, or superior to, the financial information prepared and presented in accordance with GAAP.

We define "Non-GAAP gross profit" as GAAP gross profit, adjusted for stock-based compensation expense and amortization of acquired intangible assets included in cost of revenue. We define “Non-GAAP operating expenses” as GAAP operating expenses, adjusted for stock-based compensation and amortization of acquired intangible assets. We define “Non-GAAP operating income (loss)” as GAAP operating income (loss) adjusted to exclude stock-based compensation and amortization of acquired intangible assets.

The above items are excluded from our Non-GAAP gross profit, Non-GAAP operating expenses and Non-GAAP operating income (loss) because these items are non-cash in nature, or are not indicative of our core operating performance, and render comparisons with prior periods and competitors less meaningful. We believe Non-GAAP gross profit, Non-GAAP operating expenses and Non-GAAP operating income (loss) provide useful supplemental information to investors and others in understanding and evaluating our results of operations, as well as provide a useful measure for period-to-period comparisons of our business performance.

We define free cash flow as net cash provided by (used in) operating activities less cash used for purchases of property and equipment. We believe that free cash flow is a useful indicator of liquidity that provides information to management and investors, even if negative, about the amount of cash used in our operations other than that used for investments in property and equipment.

Appendix - GAAP To Non-GAAP Reconciliation

Operating expenses

In $ thousands	Three Months Ending: Sept 30, 2024	June 30, 2025	Sept 30, 2025
Operating expenses	21,171	23,008	24,356
Less:			
Stock-based compensation expense	4,169	4,265	4,728
Amortization of acquired intangible assets	167	167	167
Non-GAAP operating expenses	16,835	18,576	19,461

Appendix - GAAP To Non-GAAP Reconciliation

Operating income (loss)

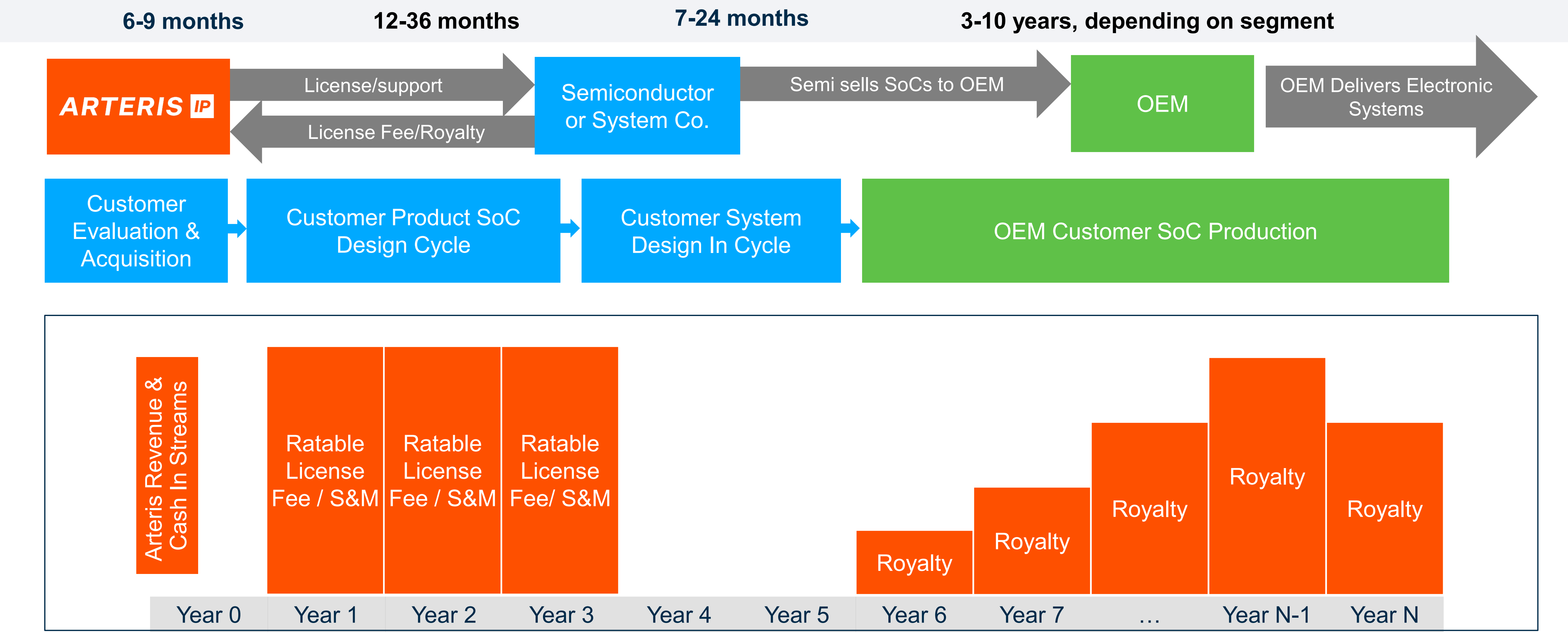
In $ thousands	Three Months Ending: Sept 30, 2024	June 30, 2025	Sept 30, 2025
Operating Income (Loss)	(7,919)	(8,248)	(8,714)
Add:			
Stock-based compensation expense	4,390	4,497	4,956
Amortization of acquired intangible assets	217	217	217
Non-GAAP Operating Income (Loss)	(3,312)	(3,534)	(3,541)

Appendix –Free Cash Flow

In $ thousands	Three Months Ending: Sept 30, 2024	June 30, 2025	Sept 30, 2025
Net cash provided by (used in) operating activities	1,124	(2,485)	3,187
Less:			
Purchase of property and equipment	(31)	(355)	(716)
Free cash flow	1,093	(2,840)	2,471

Appendix - Arteris Business Model

Arteris customers have delivered over 3.9 Billion units based on over 900 SoC projects



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Appendix - License Revenue / ACV / RPO Model

Example $3M deal, 36-month license term