

Accelerating The Creation of Semiconductors

Corporate Overview

Charlie Janac CEO Nick Hawkins CFO

ARTERIS

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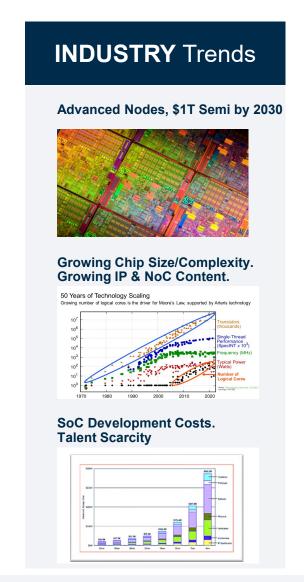
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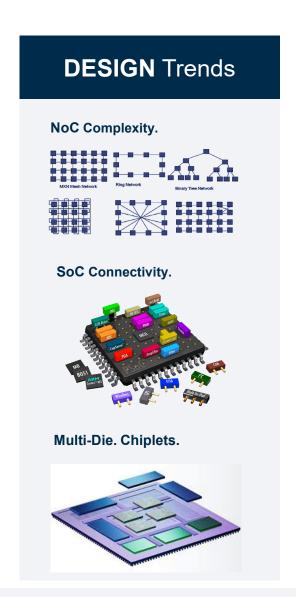
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Macro Factors Driving Arteris Growth

Growth Opportunities

MACRO Trends **Artificial Intelligence. Machine** Learning. Gen Al. LLMs. **Electrification. Energy Efficiency.** Green Tech. Sustainability. **Regionalized & Government** Investments.





SoCs are made from IP Blocks → With Increasing Complexity

Printed circuit boards became chips, chips become chiplets → SoC/SIPs become electronic systems

Arteris Network-on-Chip IP connecting:

- Non-Coherent NoC → FlexNoC/FlexWay
- Cache Coherent NoC → Ncore
- Last-Level Cache → CodaCache

Subsystem NoCs

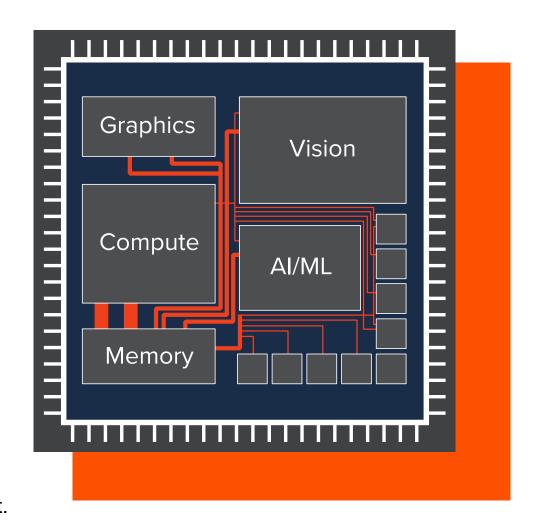
- Memory Subsystem
- NPU Subsystem
- Video Subsystem
- PCIe Subsystem
- Safety Island Subsystem

Magillem

Establishing high-level connectivity between client IP blocks

CSR-Compiler (ex Semifore)

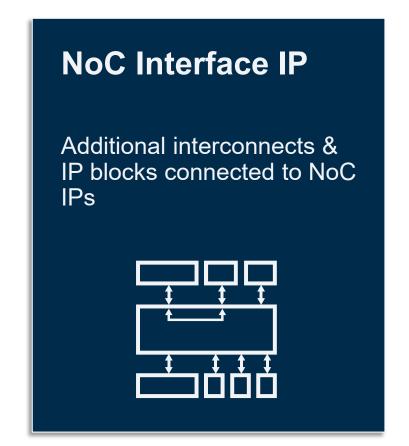
Configuring IP port register RTL for efficient Hardware Software Int.

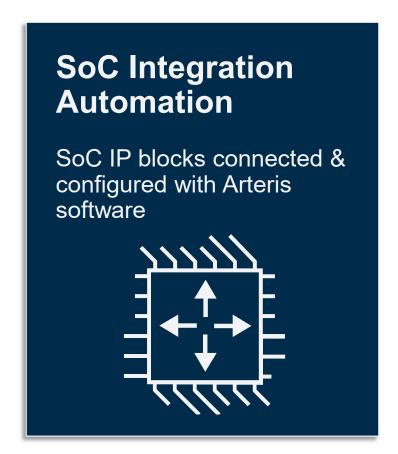


Arteris' Market Definition

Developing the System IP Market







Arteris – A Leading SoC System IP Company

Global customer base deploying Arteris interconnect IP and SoC Integration Software

- Silicon-proven IP used in ~3.5 billion+ SoCs shipped to date
- 200+ customers and 750+ SoC design starts to date
- 70-80% market share of automotive ADAS SoC market¹
- Strong market presence and product offering in Artificial Intelligence/Machine Learning (AI/ML) system IP
- Broad interoperability any processor, any IP, any EDA, any foundry
- Innovative technology coupled with expert support results in a 90+% customer retention rate
- 73 patents and 81 patent applications
- ISO 9001:2015 Quality Management System (QMS) Certified

¹ Management estimates



Technology & Economic Factors Driving Arteris' Customer Adoption

Enabling customers to create complex SoCs efficiently and reliably

Accelerated SoC Creation

Improved
SoC
Economics,
Performance

Novel SoC Architecture Support Lower Power, Smaller Die-Size



Faster time to market and shorter time to revenue

Reduction of customer R&D cost and SoC unit cost

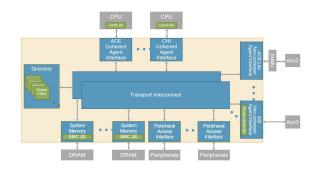
Creation of more complex, differentiated SoCs

Create lower cost, more market-attractive SoCs

2023 Technology Achievements



FlexNoC 5.0
Physically Aware NoC IP
(June)



Ncore 3.6
ARMv9 Support
(December)





ISO 9001



ISO 26262

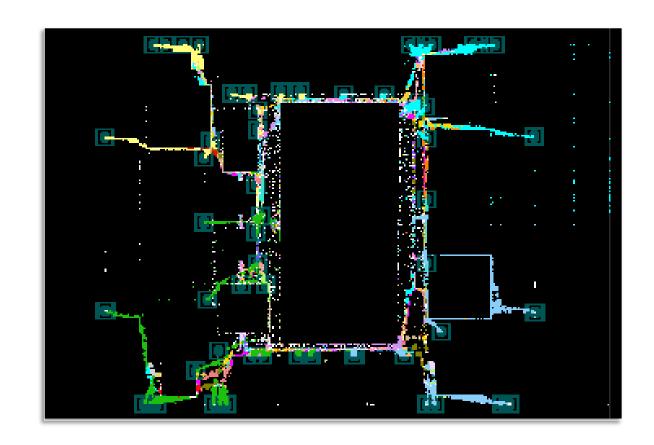
Ncore 3.4 Certification (October)

Magillem TCL1 Re-Certification (November)

Arteris Product Innovation: FlexNoC 5 Network-on-Chip IP

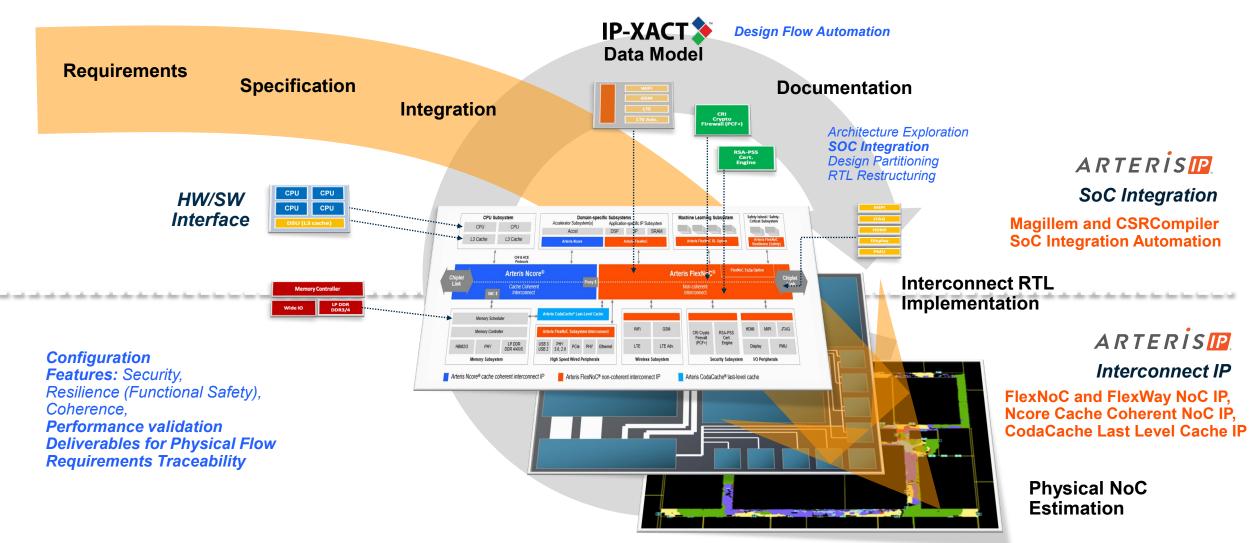
One of the Industry's First *Physically Aware* **NoC IP** Designed To Reduce Complexity +
Optimize Design

- Customers get to physical convergence up to 5X faster.
- Engineering resources are optimized, reducing manual iterations, cutting months to days.
- 3. Power, performance, and area (PPA) goals are achieved within schedule and budget constraints.
- 4. Solid customer adoption YTD
 - 21 FN5 customers through 4Q23
 - Numerous FN5 evaluations



Accelerating SoC Delivery Capacity via Advanced System IP

Arteris NoC interconnect IP and SoC Integration Automation for SoC Project Success



Arteris Has Deep Moats

Marketable Product

Annual product releases + new product innovation

Competition: 2 to 5 years to develop & mature

Product Design In

Customer SoC design win into electronic systems

Competition: 3 to 6 years to deploy

Royalty Engine

Generating royalties over a decade

Competition: 5 to 8 years to develop & mature

Investment

Established products supported by market leading expertise

Competition: \$100M+ dollars for full product line

Ecosystem

Foundry, IP & EDA ecosystem established

Competition: Lengthy development

Engineering Resources

Global team

Competition: Deep expertise, difficult to find

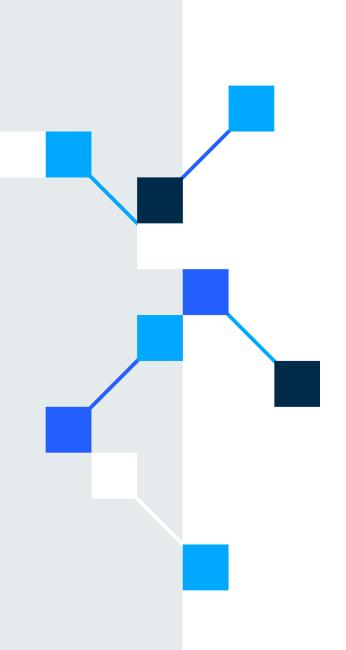
Intellectual Property

73 patents issued, 81 pending

Competition: Patent infringement risk

Total time to a solid market position:

10 to 20 years

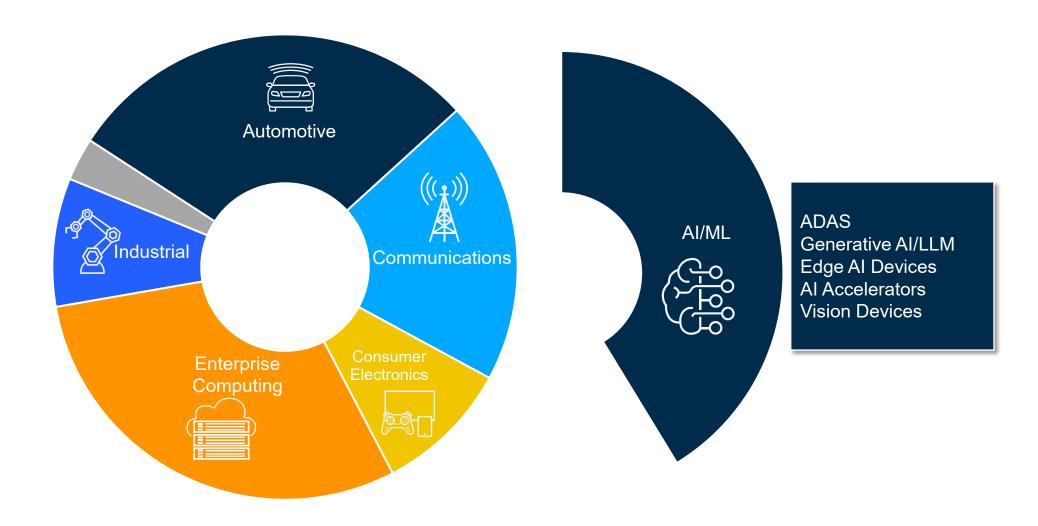


Market Segments

Addressing the most attractive segments

Accelerating Innovation in Key Verticals

2023 Revenue Vertical Breakdown



Artificial Intelligence/Machine Learning (AI/ML) Horizontal

Al Semiconductor Market: ~7x Increase from 2023 to 20301



2023 Al-Semiconductors: \$105B¹

2030 Al-Semiconductors: \$780B¹

Generative Al 56% CAGR²

- Generative AI is a revolution in man/machine interface technology → productivity revolution
- Generative AI ASICs & accelerators require high data bandwidth & unique features
- Moving data through system IP a key competency
- Many Generative AI ASICs using Arteris system IP

Arteris Customers Include

Data Center
Hyperscalers
(subject to NDAs)
Bitmain
Neureality

Rain Al

Edge Renesas NXP Sima.ai Axelera Endpoint
Mobileye
Socionext
Hailo

1. Source: IBS 2. CAGR 2023 – 2028; Source: Omdia Informa

Arteris and Rain Al Generative Al and Edge Al

Driver (Market Need)

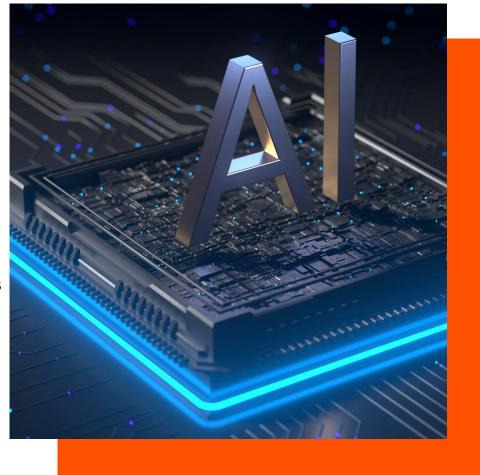
 Delivering Generative AI and Edge AI computing at scale requires faster data transfer with ultra-lower power.

AI/ML Use Case (or AI/ML Application)

 Arteris FlexNoC 5 will provide on-chip connectivity, supporting the design of an advanced mesh network topology for superior performance for Rain Al's digital in-memory compute for Al workloads

"The AI problem is an energy problem. Creating a future with abundant and scalable artificial intelligence is critical for the AI revolution. the right NoC is critical for AI computing and Arteris FlexNoC 5 was an easy choice given its unmatched product performance including ultra-low power, lowest latency, and highest bandwidth, along with deep expert support and proven track record in reducing time to market."

William Passo, CEO of Rain Al



Automotive Vertical





ADAS 31% CAGR¹

Auto HPC (High Performance Compute) 99% CAGR¹

Automotive SoC 17% CAGR²

Infotainment / Telematics SoC 15% CAGR²

- 130+ SoC design wins across both EV and ICE; OEMs take on silicon
- ~\$13 billion automotive SoC market by 2027²
- Partnered with **Arm** in Automotive
- Selected by 5 major Auto OEMs in 2023
- Unique Safety capabilities

Arteris Customers Include:

BMW

NXP

Mobileye

Dreamchip

Many others subject to NDA

CAGR 2022 – 2030; Source: Gartner
 CAGR 2023 – 2027; Source: Omdia Informa

Arteris' System IP Position in Automotive Transformation

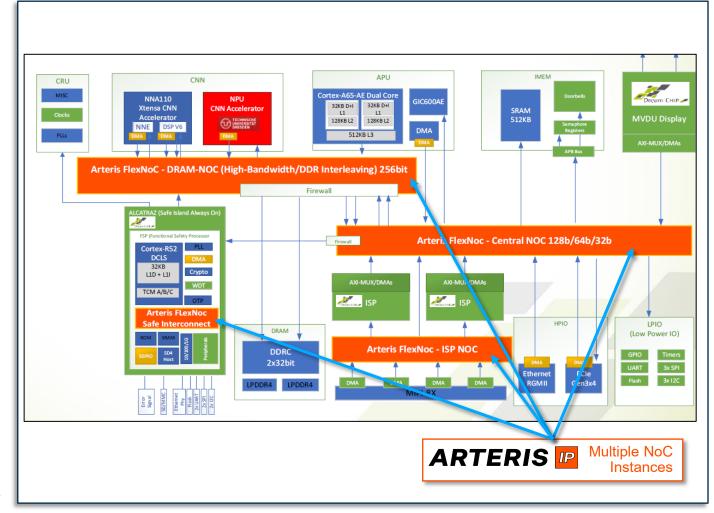
Arteris' System IP enables novel automotive SoC architectures

	SoCs per Function	Arteris IP Customers → 130+ SoC design wins				
ADAS / Machine Learning / Car Controller (1-4 systems per vehicle)	2	Horizon Robotics Major Automotive EV OEM HALL SESAME TELECTRIC Major Design Services Company Major ADAS System Maker Major ADAS System ADAS				
Vision Camera – Local Processing (4-16 systems per vehicle)	4	mobileye				
Radar / Lidar	6	Orbe Xcalterah Norde divayyar				
Infotainment	1	Major FPGA Company #1 Major FPGA Company #2 Major FPGA Company #2				
Dashboard / HUD / DMS	2	Siengine Siengine Siengine Siengine Siengine Sienes				
Chassis / Engine / Motor Control	5	life.augmented Indie				
V2X / V2I / WAN Modem / Gateway	3	Nipe Gutotalks MORNINGCORE SEQUANS SILICON Mobility				
Source: IHS Markit, McKinsey	average complex SoCs per electronically-enabled vehicle by 2026					

DreamChip Automotive Al SoC, Connected by Arteris

Production Level 2+ ADAS SoC with 2 machine learning sections

- Multiple Arteris FlexNoC Network-on-Chip instances
- 2 Al accelerators with 10 TOPS aggregated performance
- 2 DreamChip Image Signal Processors (ISPs)
- Arm Cortex-R52 based functional safety processor
- Dual-core Arm Cortex-A65AE processor cluster
- TÜV Süd certified for ISO26262:2018, up to ASIL-D



Source: https://www.eenewseurope.com/en/china-owned-dream-chip-tapes-out-10tops-soc/

Arteris Automotive Functional Safety ISO 26262 Certification for Ncore IP and Magillem

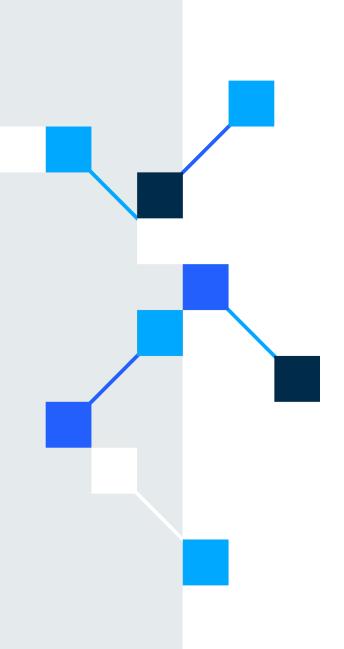
Driver (Market Need)

 Reduce time and resources required to affirm functional safety for automotive technology products

Impact

- Functional safety certification is critical for advanced driver assistance systems (ADAS) at the intersection of AI and automotive innovation
- Magillem software and Ncore, ISO 26262 certification simplifies the chip certification process for design teams, ensuring the highest functional safety standards in the automotive industry, from ASIL B to ASIL D requirements

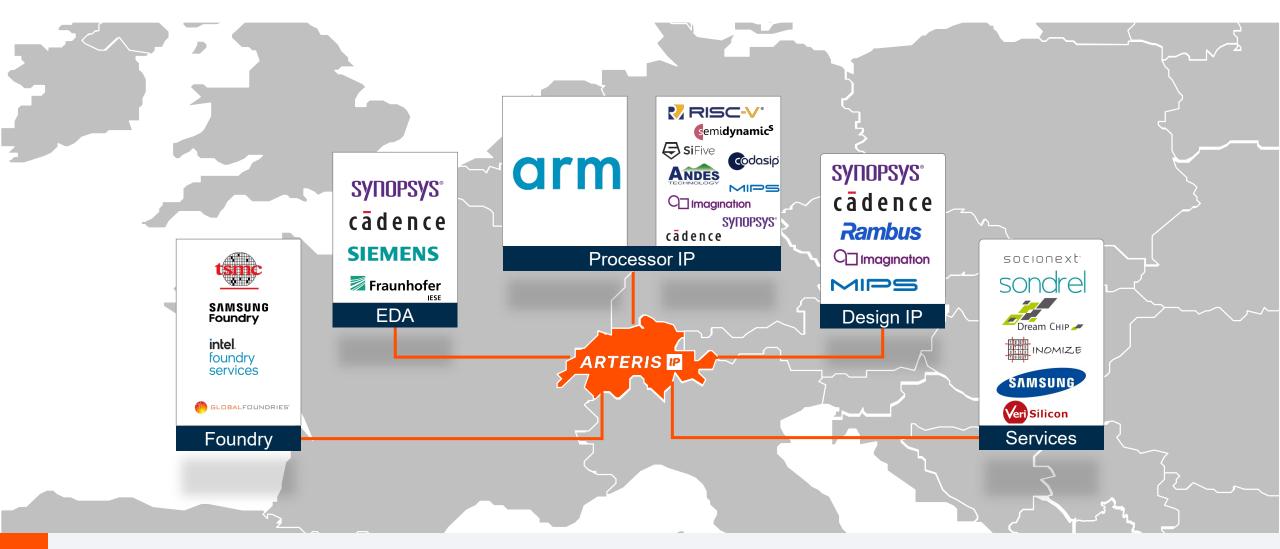




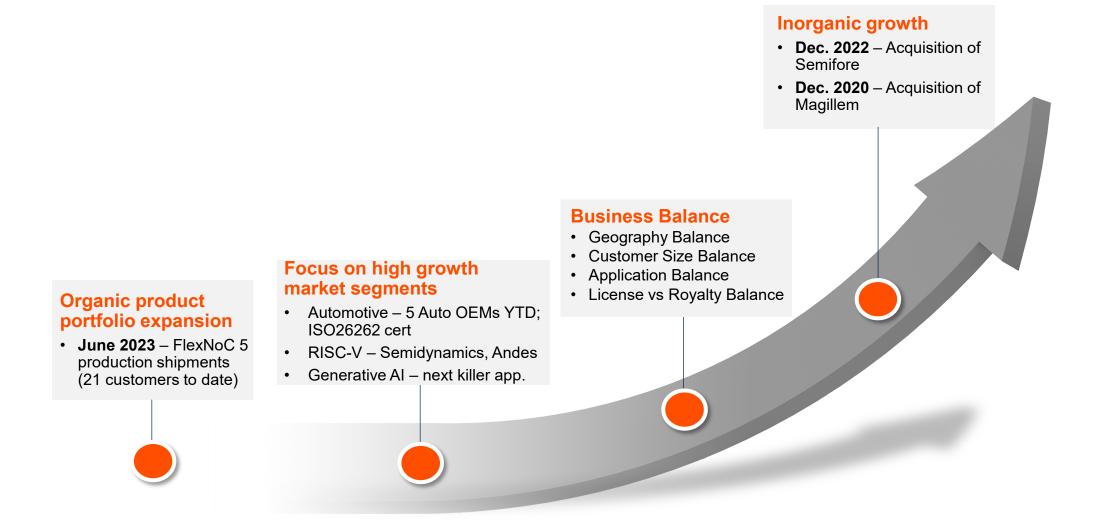
Growth Strategies

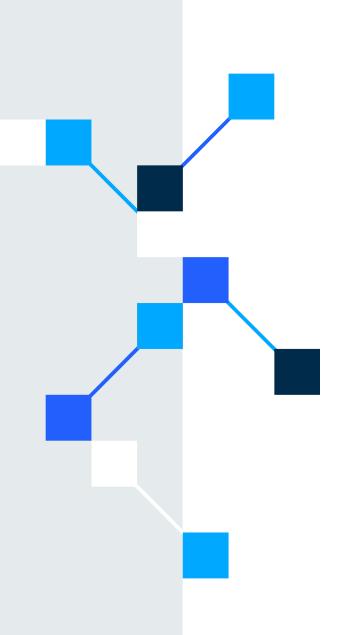
Arteris Ecosystem: The 'Switzerland' of IP

Neutral, trusted partner for IP providers and semiconductor makers



Arteris Growth Strategies and Milestones

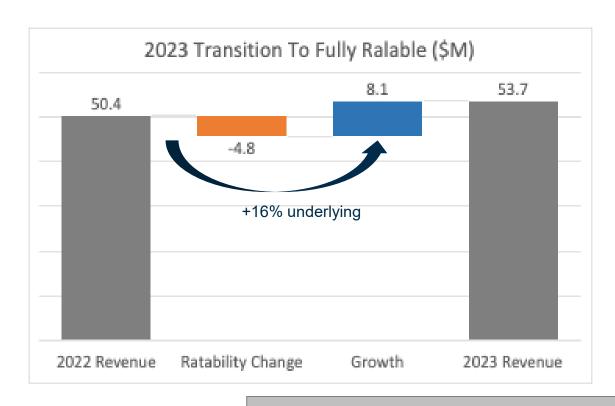




Financial Overview

Nick Hawkins

Revenue – Continued Growth





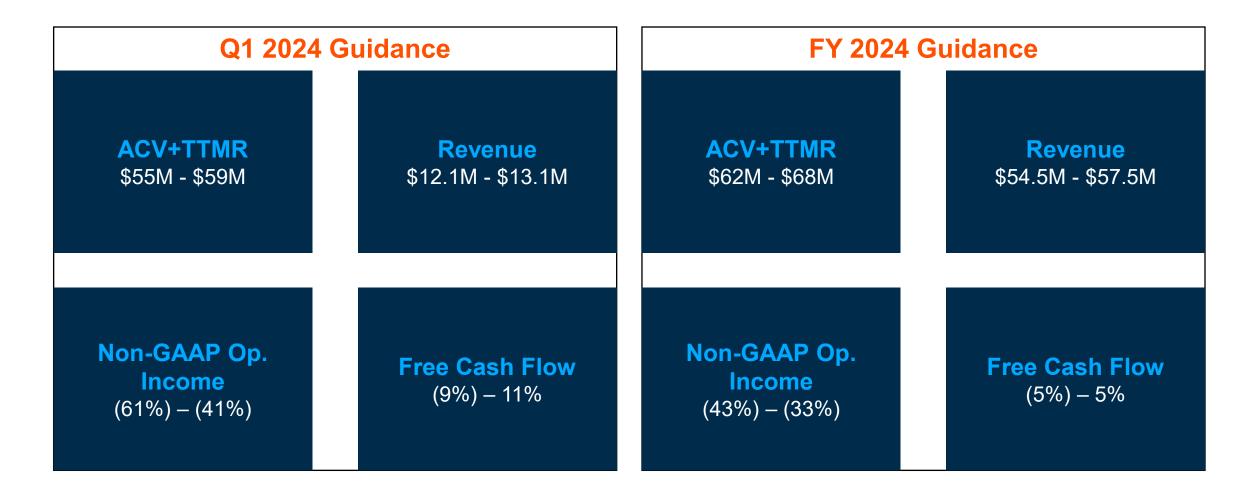
- Lower China deals (starting 3Q'23) created ~\$2M headwind to 2023 revenue, \$4M in 2024
- 4Q '23 ACV+TTMR = \$56.1M, supporting 2024 \$56.0M revenue guidance

Business Highlights

	Q4'22	Q4'23	Y/Y	FY22	FY23	Y/Y
ACV+TTMR	52.4	56.1	▲ 7%	52.4	56.1	▲ 7%
Revenue	11.2	12.5	▲12%	50.4	53.7	▲ 7%
CoR	(1.0)	(1.2)	▲23%	(3.7)	(4.4)	▲ 17%
GM%	91%	90%	▼-1%	93%	92%	▼-1%
NG OpEx	(16.0)	(16.8)	▲ 5%	(62.8)	(69.1)	▲10%
NGOI	(5.8)	(5.5)	▲ 5%	(16.2)	(19.8)	▼-22%
Free Cash Flow	(8.0)	(3.4)	▼-325%	(7.8)	(17.2)	▼-121%
RPO	57.8	72.7	▲ 26%	57.8	72.7	▲ 26%
Confirmed Design Starts	24	29	+5	82	95	+13

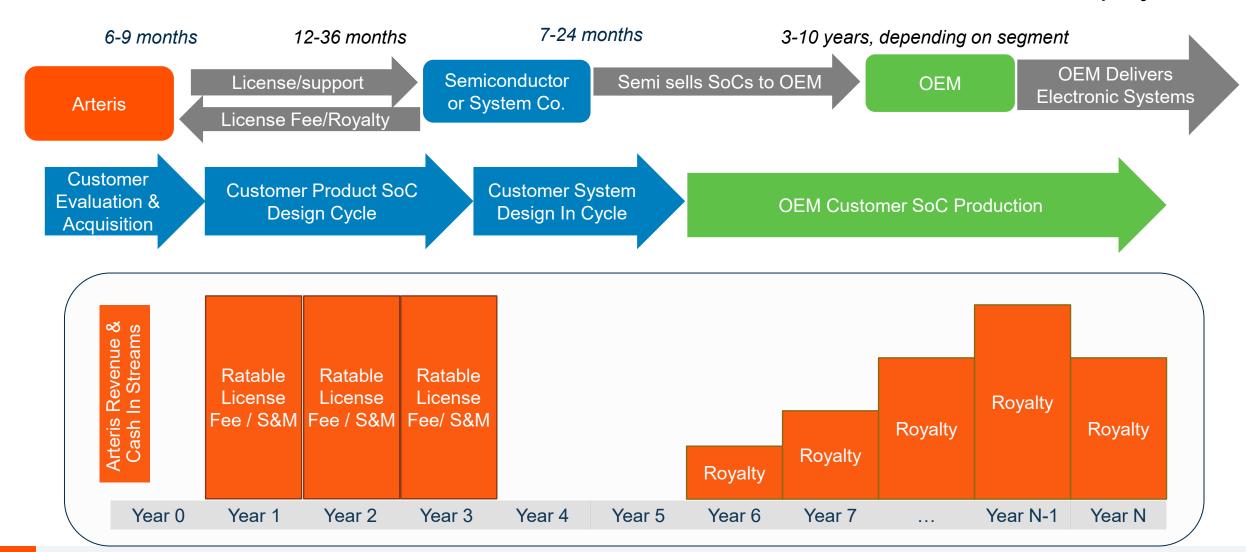
- ACV+TTMR up 7% and Q4 Revenue up 12% Y/Y
- Highest RPO at 72.7M; 26% Y/Y increase
- 21 customers licensed FlexNoC 5 to date, with numerous additional prospects and evaluations
- 95 confirmed design starts highest annual design activity
- Momentum in key verticals
 - Enterprise Computing and AI/ML:
 - NeuReality, Rain AI Generative AI, Edge AI and LLM inference
 - Communications:
 - EdgeQ, SCALINX 5G Infrastructure and Al Communications
 - Automotive:
 - ISO26262 certification for Ncore IP and Magillem software

FY2024 Guidance



Arteris Business Cycle

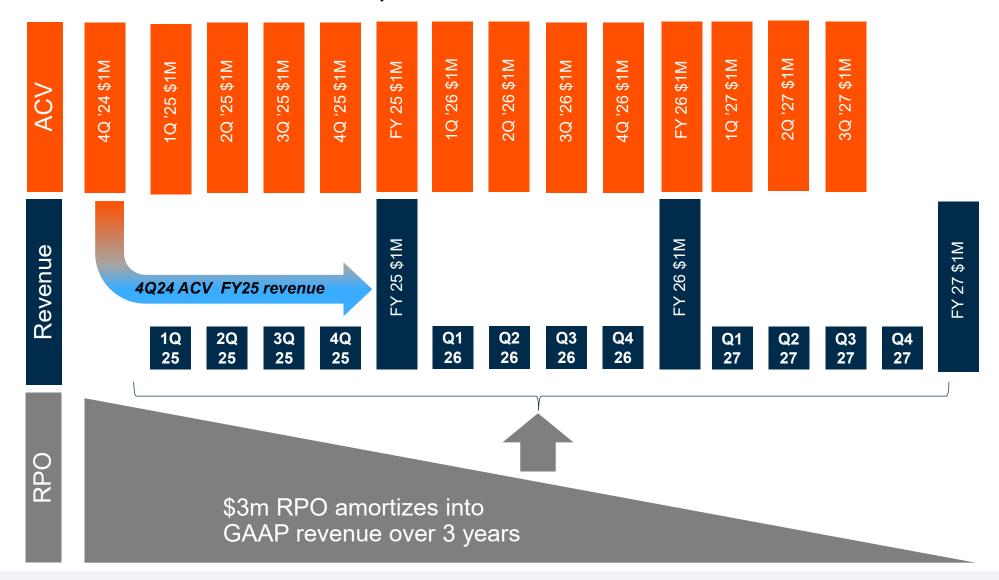
Arteris Customer base has delivered over \$3.5 Billion SoCs based on 750+ SoC projects



License Revenue / ACV / RPO Model

\$3M / 3 Yr Deal Signed 12/29/24

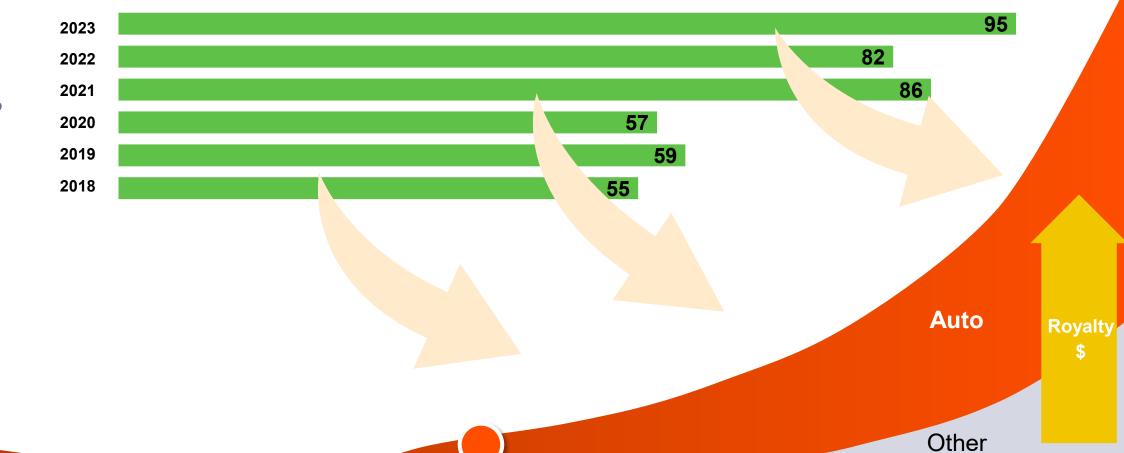
Example \$3M Deal, 36-Month License Term



Growth in Confirmed Design Starts Drives Future Royalty Expansion

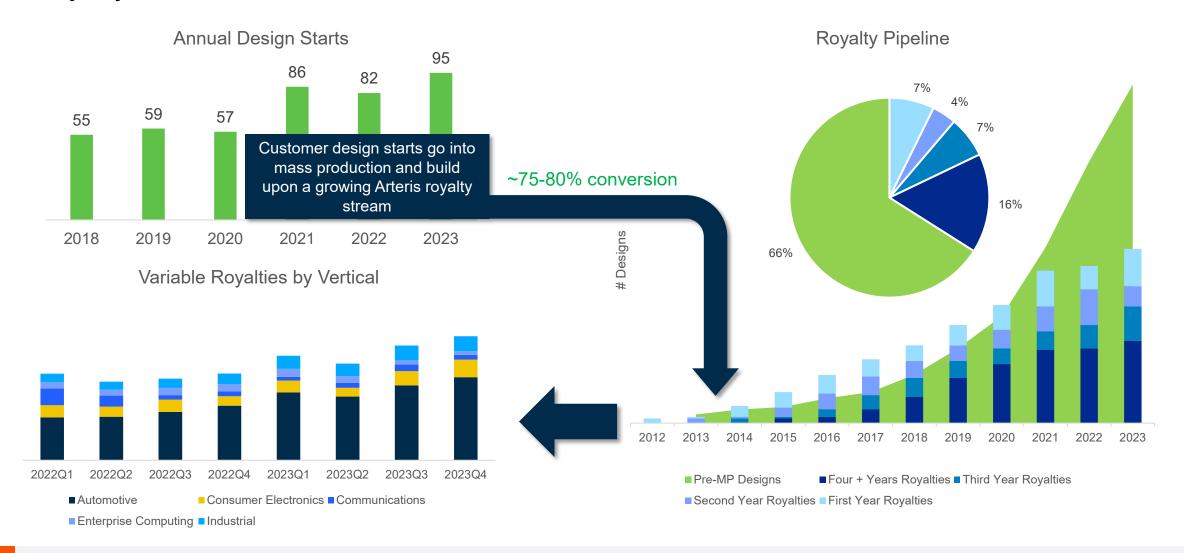
Confirmed Design Starts³





³ We define Confirmed Design Starts as when customers confirm their commencement of new semiconductor designs using our interconnect IP and notify us

Arteris Business Model Royalty Stream



2023 Actuals vs Guidance

In \$ millions	Q4 2023 Guidance	Q4 2023 Results	FY 2023 Guidance	FY 2023 Results	
ACV + TTM ² Royalties	52.0 – 56.0	56.1	52.0 – 56.0	56.1	Exceeded top end of guidance range
Revenue	11.3 – 12.3	12.5	52.5 – 53.5	53.7	Exceeded top end of guidance range
Non-GAAP Operating Income ¹		(5.5)		(19.8)	
% Revenue	(76.1%) – (56.1%)	(44%)	(44.0%) – (39.0%)	(37%)	Better than top end of guidance
Free Cash Flow		(3.4)		(17.2)	
% Revenue	(52.4%) – (32.4%)	(27%)	(38.1%) – (33.1%)	(32%)	Better than top end of guidance

¹ See appendix for reconciliation of GAAP to non-GAAP ² Represents TTM (Trailing Twelve Months)

2023: Arteris' Year In Numbers



200+

Active Customers

750+
Design Starts

\$73M +26% Y/Y RPO¹

90-95%Gross Margin²

3.5BSoCs shipped to date

High Growth Markets

Automotive, Enterprise Computing, Communications, Consumer, Industrial + AI/ML across all verticals

¹RPO: "Remaining Performance Obligations." We define this as the amount of contracted future revenue that has not yet been recognized, including deferred revenue, billed and unbilled cancelable and non-cancelable contracted amounts.

22020-2023

Investor Highlights

Market Leadership

- Leader in semiconductor System-on-Chip (SoC) System IP
- Over 50% increase in Active Customers since 2020
- 750+ SoC Design Starts
- ~3.5 billion SoCs shipped

Differentiated Technology

- Networking technology inside semiconductors
- Strong SoC Integration technology, IP-XACT committee member
- Global customer support
- 73 issued patents and 81 patent applications

Well-Positioned in High Growth Segments

- 70 80% market share of automotive ADAS SoC market¹
- Autonomous vehicle/ADAS TAM growing at 36% CAGR through 2030²
- Strong position in AI/ML system IP with over 200 design wins

Large Addressable Market

- \$1 trillion semiconductor market by 2030³
- Demand for system IP growing due to greater SoC complexity

Scalable Business Model

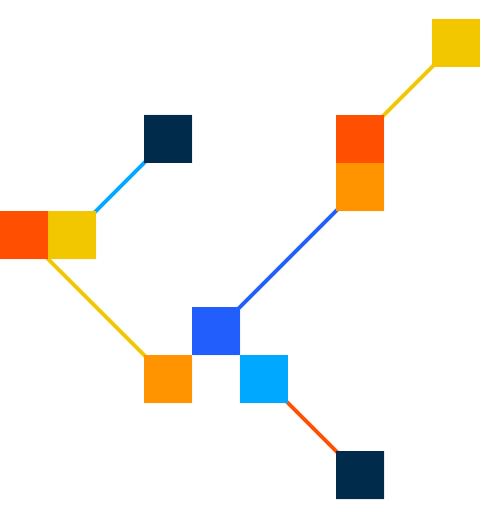
- IP business model
- Address high-growth segments with growing royalty streams
- Targeting high operating margin
- ~\$73 million contracted future revenue (RPO)⁴

¹ Management estimates

² Deutsche Bank Research

³ IBS

⁴ As of December 31, 2023. We define this as the amount of contracted future revenue that has not yet been recognized, including deferred revenue, billed and unbilled cancelable and non-cancelable contracted amounts.





Thank you

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Appendix - GAAP to Non-GAAP Reconciliation Income (loss) from operations

	Twelve Mon	ths Ending	Three Months Ending:		
In \$ thousands	December 31, 2022	December 31, 2023	December 31, 2022	December 31, 2023	
Income (loss) from Operations	(28,856)	(35,142)	(9,060)	(9,249)	
Add:					
Stock-based Compensation	11,692	14,535	2,610	3,554	
Acquisition costs	527		527		
Amortization of acquired intangible assets	478	767	120	192	
Non-GAAP income (loss) from operations	(16,159)	(19,840)	(5,803)	(5,503)	