Arteris® IP FlexNoC® Interconnect and Resilience Package Licensed in Neural Network Accelerator Chip Project Led by BMW Group

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NoC interconnect IP to be dataflow backbone of German Federal Ministry of Education and Research (BMBF) project chip to advance automotive artificial intelligence and machine learning (AI/ML) processing

CAMPBELL, Calif., April 05, 2022 (GLOBE NEWSWIRE) -- Arteris IP (NASDAQ: AIP), a leading provider of system-on-chip (SoC) system IP consisting of network-on-chip (NoC) interconnect and IP deployment software that accelerate SoC creation, today announced that BMW Group has licensed FlexNoC interconnect IP and the accompanying FlexNoC Resilience Package IP for use in a chip partially funded by the German Federal Ministry of Education and Research (BMBF) as part of the ZUSE-KI-mobil publicly funded project. The goal of the project is to develop an accelerator chip for high-end deep learning applications that is a leap forward in terms of energy efficiency, reliability, robustness and security, which go far beyond current possibilities. BMW Group is serving as the coordinator and leader of the project.

Arteris FlexNoC interconnect and Resilience Package IP has been chosen because it includes critical technologies that enhance system-level functional safety, reliability, energy efficiency and security while also increasing system performance. Creating a system that balances huge bandwidth AI/ML processing requirements and tight real time latency deadlines is already difficult, and achieving stretch goals for energy efficiency, reliability and security creates another order of magnitude of challenges. Arteris IP's advanced technology was the only interconnect IP option that allowed the project to create a leap in the state-of-the-art of automotive dedicated machine-learning SoCs.

"We are excited that the consortium led by BMW Group chose Arteris IP interconnect technology as the on-chip communications on-chip network for this innovative automotive system-on-chip," said K. Charles Janac, president and CEO of Arteris IP. "BMW Group's decision to use our interconnect IP as the dataflow on-chip network of this complex system is a testimony to the benefits our technologies bring to design teams creating the world's most sophisticated AI/ML processing chips."

About Arteris IP
Arteris IP provides system-on-chip (SoC) system IP consisting of network-on-chip (NoC) interconnect IP and IP deployment technology to accelerate system-on-chip (SoC) semiconductor development and integration for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers, and servers for customers such as Bosch, Baidu, Mobileye, Samsung, Toshiba and NXP. Arteris IP products include the Ncore® cache coherent and FlexNoC® non-coherent interconnect IP, the CodaCache® standalone last level cache, and optional Resilience Package (ISO 26262 functional safety), FlexNoC AI Package, and PIANO® automated timing closure capabilities. Our IP deployment products provide intelligent automation that accelerates the development and increases the quality of SoC hardware designs and their associated software and firmware, verification and simulation platforms, and specifications and customer documentation. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at https://www.linkedin.com/company/arteris.

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