



## Pankaj Mayor Joins Arteris IP as Executive Vice President of Global Sales

March 1, 2022

**Veteran executive with extensive semiconductor, IP and EDA experience to lead the company through the next stage of growth**

CAMPBELL, Calif., March 01, 2022 (GLOBE NEWSWIRE) -- Arteris IP (NASDAQ: AIP), a leading provider of system-on-chip (SoC) system IP consisting of [network-on-chip \(NoC\) interconnect](#) and [IP deployment software](#) that accelerate SoC creation, today announced that Pankaj Mayor has joined the company as its Executive Vice President of Global Sales. Mr. Mayor will have worldwide responsibility for sales, field engineering and customer support and will be a key member of the Arteris IP executive team.

Mr. Mayor has previously held executive roles at Cadence, Ansys and GlobalFoundries. Most recently, he was Senior Vice President of Sales and Marketing at Omni Design Technologies, an emerging semiconductor IP company. At Ansys, he was Vice President of Worldwide Marketing and led the creation of several business/OEM partnerships. At GlobalFoundries, as Vice President of Business Development, he led the development of the China strategy, including a joint venture fab. At Cadence, he was a member of the executive management team and held a variety of executive marketing, sales operations, and business positions. He was also an early employee at Synopsys.

"I am excited to join Arteris IP as the company is entering a new phase of growth," said Pankaj Mayor, executive vice president of global sales at Arteris IP. "System IP is becoming critical to our customers' ability to deliver complex SoCs on schedule and on budget. I look forward to working with the Arteris IP team to make our customers successful."

"We are honored to welcome Pankaj Mayor to our executive team," said K. Charles Janac, president and CEO of Arteris IP. "Pankaj has broad experience in EDA, semiconductors and semiconductor IP, which will help Arteris IP achieve its growth objectives as a public company and as a leader in the system IP market."

### About Arteris IP

Arteris IP (NASDAQ: AIP) provides system-on-chip (SoC) system IP consisting of [network-on-chip \(NoC\) interconnect IP](#) and [IP deployment technology](#) to accelerate SoC semiconductor development and integration for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers and servers for customers such as [Bosch](#), [Baidu](#), [Mobileye](#), [Samsung](#), [Toshiba](#) and [NXP](#). Arteris IP products include the [Ncore](#)® cache coherent interconnect IP and [FlexNoC](#)® non-coherent interconnect IP, the [CodaCache](#)® standalone last level cache, and optional [Resilience Package \(ISO 26262 functional safety\)](#), [FlexNoC AI Package](#), and [PIANO](#)® automated timing closure capabilities. Our [IP deployment products](#) provide intelligent automation that accelerates the development and increases the quality of SoC hardware designs and their associated software and firmware, verification and simulation platforms, and specifications and customer documentation. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit [www.arteris.com](http://www.arteris.com) or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

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### Contact:

Michele Kinman  
The Edge Marketing  
+1 408-218-8815  
[mkinman@the-edgemarketing.us](mailto:mkinman@the-edgemarketing.us)

A photo accompanying this announcement is available at <https://www.globenewswire.com/NewsRoom/AttachmentNg/029963fa-7be2-4e15-ad4b-d45daf5d35f0>

Pankaj Mayor



Executive Vice President of Global Sales