



ArterisIP Joins GLOBALFOUNDRIES FDXcelerator Partner Program

October 10, 2017

Program enables faster development of automotive and IoT systems-on-chip (SoC)

CAMPBELL, Calif. —October 10, 2017 — ArterisIP, the innovative supplier of silicon-proven commercial system-on-chip (SoC) interconnect IP, today announced it has joined the GLOBALFOUNDRIES FDXcelerator™ Partner Program. This program enables SoC designers to integrate ArterisIP interconnect IP into their projects with the ability to accelerate the timing closure process for FDX-based designs. The partnership speeds the development of pioneering products in applications from automotive ADAS and machine learning to small IoT processors.

"The addition of ArterisIP to the FDXcelerator Partnership Program has already realized benefits with the implementation of an FD-SOI automotive ADAS multi-processor SoC with fellow FDXcelerator partner Dream Chip Technologies.

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Alain Mutricy, Senior Vice President of Product Management, **GLOBALFOUNDRIES**

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ArterisIP offerings participating in the FDXcelerator program include:

- The [Ncore Cache Coherent Interconnect IP with Ncore Resilience Package](#), which has been chosen by the industry's leading automotive ADAS, autonomous driving, and machine learning SoC vendors for its power, performance, and area advantages and ISO 26262 functional safety features.
- The [FlexNoC Interconnect IP](#) with [FlexNoC Resilience Package](#), which is the backbone interconnect for most mobility and consumer electronics SoC designs where power consumption, performance, and cost are key design metrics.
- The [PIANO Timing Closure Package](#), which assists back-end timing closure with technology that works earlier in the SoC design flow, thereby reducing schedule risk.

"The addition of ArterisIP to the FDXcelerator Partnership Program has already realized benefits with the implementation of an FD-SOI automotive ADAS multi-processor SoC with fellow FDXcelerator partner Dream Chip Technologies," said Alain Mutricy, senior vice president of product management at GF. "ArterisIP's commitment to GF's FDX technology enables a scalable on-chip interconnect IP technology that will help our customers meet stringent automotive safety requirements."

"GF's FDXcelerator program plays an important role for ArterisIP, enabling us to gain access to FD-SOI technology process and design information to enable improved automation of our interconnect timing closure assistance technology," said K. Charles Janac, President and CEO of ArterisIP.

"Interconnect timing closure assistance is becoming imperative as technologies like FD-SOI shrink feature sizes and allow ever-increasing transistor and wire densities."

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About ArterisIP

ArterisIP provides [system-on-chip \(SoC\) interconnect IP](#) to accelerate SoC semiconductor assembly for a wide range of applications from automobiles to mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Samsung](#), [Huawei / HiSilicon](#), [Mobileye \(Intel\)](#), [Altera \(Intel\)](#), and [Texas Instruments](#). ArterisIP products include the [Ncore](#) cache coherent and [FlexNoC](#) non-coherent interconnect IP, as well as optional [Resilience Package \(ISO 26262 functional safety\)](#) and [PIANO automated timing closure](#) capabilities. Customer results obtained by using the ArterisIP product line include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at www.linkedin.com/company/arteris.

Editorial Contact

Kurt Shuler
Arteris Inc.
+1 408 470 7300
kurt.shuler@arteris.com

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