Arteris IP FlexNoC® Interconnect Licensed by AutoChips for Automotive SoC Development

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Chinese semiconductor supplier chooses Arteris FlexNoC to help accelerate automotive SoC development and ISO 26262 functional safety certification

CAMPBELL, Calif. — February 272018 — Arteris IP, the innovative supplier of silicon-proven commercial system-on-chip (SoC) interconnect semiconductor intellectual property (IP), today announced that Chinese automotive semiconductor vendor AutoChips has licensed Arteris FlexNoC interconnect IP as the on-chip communications backbone of its next-generation automotive SoC chip.

> *"We chose Arteris FlexNoC IP because it is the first and only on-chip interconnect IP with a track record of mass production automotive chip implementations at the ISO 26262 ASIL B levels.*
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> autochips-logo-chinese.png
> *WenHsin Wang, General Manager,* ***AutoChips***

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Arteris FlexNoC interconnect IP is the on-chip communications backbone of most of the world's automotive ADAS SoCs, application processors, digital baseband modems, and enterprise SSD controllers. The FlexNoC Resilience Package adds data protection features required to obtain higher ISO 26262 automotive safety integrity levels (ASIL) by implementing on-chip error code correction (ECC) protection, hardware redundancy, unit checking, data monitoring and built in self-test (BIST), and by providing a complete set of ISO 26262 documentation.

"We chose Arteris FlexNoC IP because it is the first and only on-chip interconnect IP with a track record of mass production automotive chip implementations at the ISO 26262 ASIL B levels," said WenHsin Wang, General Manager at AutoChips.

Arteris IP is the leading interconnect IP company with proven technology and expertise that helps automotive semiconductor companies deliver higher performing and safer chips with lower schedule and development risk.

"We are proud that AutoChips has chosen Arteris IP FlexNoC as the on-chip communications backbone of its new SoC," said K. Charles Janac, President and CEO of Arteris IP. "This selection is further validation that Arteris IP helps accelerate product development and ISO 26262 certification of highly complex automotive systems."

About Arteris IP

Arteris IP provides system-on-chip (SoC) interconnect IP to accelerate SoC semiconductor assembly for a wide range of applications from automobiles to mobile phones, IoT, cameras, SSD controllers, and servers for customers such as Samsung, Huawei / HiSilicon, Mobileye (Intel), Altera (Intel), and Texas Instruments. Arteris IP products include the Ncore cache coherent and FlexNoC non-coherent interconnect IP, as well as optional Resilience Package (functional safety) and PIANO automated timing closure capabilities. Customer results obtained by using the Arteris product line include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at https://www.linkedin.com/company/arteris.

About AutoChips

AutoChips Inc., the predecessor of which is the world leading IC company MediaTek, is a wholly-owned subsidiary of NavInfo. AutoChips' head office is based in Hefei, responsible for designing and developing, and has two branch offices located in Shenzhen and Shanghai.

AutoChips is engaged in research and SoC design of automotive electronics, and is awarded the qualifications of "National Hi-Tech company" and "National IC design company". AutoChips first-class engineering and service make it the leading position in the China after-market. Its products are widely used by Tier 1 customers and automotive manufacturers.

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