



## Arteris IP Supports Dream Chip Technologies Innovative Business Model for Automotive SoC Development

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Automotive design services leader offers automotive SoC reference design IP enabled by flexible, ISO 26262-compliant FlexNoC on-chip interconnect

CAMPBELL, Calif. —March 7, 2018 — Arteris IP, the innovative supplier of silicon-proven commercial system-on-chip (SoC) [interconnect semiconductor intellectual property \(IP\)](#), today announced its support of Dream Chip Technologies' pioneering business model to accelerate the development and deployment of next-generation automotive SoCs for autonomous driving, advanced driver assistance systems (ADAS), LIDAR controllers and radar controllers.

*"Using Arteris FlexNoC IP as the on-chip interconnect for our reference designs allows us to quickly customize each platform for our customers' performance, power, and ISO 26262 functional safety goals, while also providing a simple path to create follow-on derivative designs."*



Dr.-Ing. Jens Benndorf, Managing Director and COO, **Dream Chip Technologies**

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Dream Chip Technologies GmbH (DCT) is the largest independent German semiconductor design service company. The company specializes in the creation of highly complex systems-on-chip for the automotive market. DCT offers for license automotive reference development platforms that incorporate the Arteris IP [FlexNoC interconnect](#) with the [FlexNoC resilience package](#) as the communications backbone of the SoC designs. These chip-as-an-IP reference designs have been licensed by leading companies who have decades of automotive experience and are interested in benefiting from DCT's semiconductor expertise.

Dream Chip Technologies has pioneered the licensing of SoC platforms as semiconductor IP. Certain automotive and industrial customers lack the time, expertise or resources to design a chip, yet need the performance, power and feature benefits that only the customization of a tailored SoC can provide. For these customers, Dream Chip Technologies offers a complete and proven SoC platform that can be customized for specific applications with a fraction of the resources and schedule needed for a full SoC design created from the ground up.

"Using Arteris FlexNoC IP as the on-chip interconnect for our reference designs allows us to quickly customize each platform for our customers' performance, power, and ISO 26262 functional safety goals, while also providing a simple path to create follow-on derivative designs," said Dr.-Ing. Jens Benndorf, Managing Director and COO of Dream Chip Technologies. "This is particularly important to our automotive customers who want to quickly deploy and improve customized systems that accelerate their imaging, LIDAR point cloud, and 3D radar algorithms. Furthermore, our use of the FlexNoC Resilience Package helps ensure our customers can achieve the highest levels of ISO 26262 ASIL certification."

[Arteris FlexNoC interconnect IP](#) is the on-chip communications backbone of most of the world's automotive ADAS SoCs, application processors, digital baseband modems, and enterprise SSD controllers. The [FlexNoC Resilience Package](#) adds data protection features required to obtain higher [ISO 26262 automotive safety integrity levels \(ASIL\)](#) by implementing on-chip error code correction (ECC) protection, hardware redundancy, unit checking, data monitoring and built in self-test (BIST), and by providing a complete set of ISO 26262 documentation.

"The flexibility, PPA advantages, and functional safety mechanisms of Arteris FlexNoC IP help make Dream Chip Technologies' automotive reference platform business model possible and enable automotive suppliers to more quickly create optimized semiconductor systems," said K. Charles Janac, President and CEO of Arteris IP. "Dream Chip Technologies' platforms and business model allow automotive companies to implement specialized algorithmic IP in custom chips, thereby increasing system performance and safety while reducing overall cost, time to market and resource commitments."

#### About Arteris IP

Arteris IP provides [system-on-chip \(SoC\) interconnect IP](#) to accelerate SoC semiconductor assembly for a wide range of applications from automobiles to mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Samsung](#), [Huawei / HiSilicon](#), [Mobileye](#) (Intel), [Altera](#) (Intel), and [Texas Instruments](#). Arteris IP products include the [Ncore](#) cache coherent and [FlexNoC](#) non-coherent interconnect IP, as well as optional [Resilience Package \(functional safety\)](#) and [PIANO automated timing closure](#) capabilities. Customer results obtained by using the Arteris IP product line include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit [www.arteris.com](http://www.arteris.com) or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

#### About Dream Chip Technologies

Dream Chip Technologies GmbH (DCT) is the largest independent German design service company, specialized in the development of large ASICs, FPGAs, embedded software and systems, with a strong application focus on automotive vision systems. Companies worldwide from different

industrial sectors rely on DCT's expertise and outstanding engineering skills. It is DCT's mission to bridge the gap between demand and supply by supporting their customers with sophisticated vision technologies, thus enabling them to launch new products without neglecting their core business. This strategy ensures business stability and steady growth through product innovations simultaneously. DCT's headquarters are located in Northern Germany near Hanover. For more information, please visit the company's web site at: [www.dreamchip.de](http://www.dreamchip.de)

#### **Editorial Contacts**

Kurt Shuler  
Arteris IP  
+1 408 470 7300  
[kurt.shuler@arteris.com](mailto:kurt.shuler@arteris.com)

Young-Hun Kluge  
Dream Chip Technologies GmbH  
+49 5131-90805-133  
[yh.kluge@dreamchip.de](mailto:yh.kluge@dreamchip.de)

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