



Sonics and Arteris IP Agree to Dismiss Litigation

March 21, 2019

Litigation is dismissed with prejudice with no funds changing hands

CAMPBELL, Calif. —March 21, 2019 —Arteris IP, the world's leading supplier of innovative, silicon-proven [network-on-chip \(NoC\) interconnect](#) intellectual property, today announced that Case # 11-cv-5311-SBA was dismissed with prejudice by the Northern District of California Court with support of all parties involved. This litigation was launched by Sonics Inc. in November of 2011 alleging infringement of seven Sonics patents by Arteris, Inc. Arteris vigorously denied any infringement while challenging the validity of Sonics' patents. The case was stayed for patent re-examination by the US Patent and Trademark Office in June of 2014 and was dismissed in March of 2019. The stipulated dismissal with prejudice is now part of the public record.

"Arteris IP will, as always, continue to compete strongly in the interconnect IP market for the benefit of the entire semiconductor industry and our nearly 120 customers."

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K. Charles Janac, President and CEO, Arteris IP

"Arteris IP will, as always, continue to compete strongly in the interconnect IP market for the benefit of the entire semiconductor industry and our nearly 120 customers," said K. Charles Janac, President and CEO of Arteris IP.

About Arteris IP

Arteris IP provides [network-on-chip \(NoC\) interconnect IP](#) to accelerate system-on-chip (SoC) semiconductor assembly for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Baidu](#), [Mobileye](#), [Samsung](#), [Huawei / HiSilicon](#), [Toshiba](#) and [NXP](#). Arteris IP products include the [Ncore](#) cache coherent and [FlexNoC](#) non-coherent interconnect IP, the [CodaCache](#) standalone last level cache, and optional [Resilience Package \(ISO 26262 functional safety\)](#), [FlexNoC AI Package](#), and [PIANO automated timing closure](#) capabilities. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

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