



Arteris® IP FlexNoC® Interconnect Licensed by Morningcore for Automotive LTE-V2X Modems for China Market

March 26, 2019

NoC interconnect ensures high data bandwidth and low latency on-chip communications

CAMPBELL, Calif. — March 26, 2019 — Arteris IP, the world's leading supplier of innovative, silicon-proven [network-on-chip \(NoC\) interconnect](#) intellectual property, today announced that Morningcore Technology has licensed [Arteris FlexNoC interconnect IP](#) for use in its next-generation automotive LTE vehicle-to-vehicle / vehicle-to-infrastructure (V2X) communication modems.

"The Arteris team has unique expertise in both automotive and wireless applications, and their technology and local support have been a perfect fit for our development team's needs."

morningcore-logo-LinkedIn-Post
Zhigang Gu, Product Manager, **Morningcore**

Morningcore is headquartered in Shanghai, China, and has a branch in Beijing. It has mastered technology for 3G/4G/5G mobile communications terminals, software-defined radio (SDR) chips, and other large-scale integrated circuits. The company's new LTE-V2X modems will provide the wireless communications infrastructure for future autonomous driving solutions for Smart Cities.

"Arteris IP's FlexNoC interconnect is the only interconnect IP that could meet our bandwidth, latency and power requirements," said Zhigang Gu, Product Manager of Morningcore. "The Arteris team has unique expertise in both automotive and wireless applications, and their technology and local support have been a perfect fit for our development team's needs."

"Morningcore is leading the way as highway and Smart City infrastructure becomes electronic in support of the automated transportation revolution," said K. Charles Janac, President and CEO of Arteris IP. "Arteris interconnect IP technology is providing Morningcore new capabilities to meet aggressive performance and power objectives which will accelerate the deployment of the V2X segment of automated driving."

About Morningcore Technology

Morningcore Technology Co., Ltd., which is an integrated circuit chip design enterprise under the CICT, was established in March 2017, headquartered in Shanghai and has a branch office in Beijing. Morningcore provides advanced and reliable wireless communication SoC terminal chips, SDR, i.e. Software-Defined Radio, platform and solutions to the customers who are in professional fields, such as intelligent IoT, private network communication, and vehicle networking. Its core technology team has more than ten years of successful experience. Morningcore's actual controller, CATT, is one of the main drivers of C-V2X international standards.

About Arteris IP

Arteris IP provides [network-on-chip \(NoC\) interconnect IP](#) to accelerate system-on-chip (SoC) semiconductor assembly for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Baidu](#), [Mobileye](#), [Samsung](#), [Huawei / HiSilicon](#), [Toshiba](#) and [NXP](#). Arteris IP products include the [Ncore](#) cache coherent and [FlexNoC](#) non-coherent interconnect IP, the [CodaCache](#) standalone last level cache, and optional [Resilience Package \(ISO 26262 functional safety\)](#), [FlexNoC AI Package](#), and [PIANO automated timing closure](#) capabilities. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

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