



Arteris IP FlexNoC® Interconnect Licensed by DisplayLink for Systems-on-Chip

April 16, 2019

NoC interconnect enables greater chip design flexibility

CAMPBELL, Calif. –April 16, 2019– Arteris IP, the world's leading supplier of innovative, silicon-proven [network-on-chip \(NoC\) interconnect](#) intellectual property, today announced that DisplayLink has licensed [Arteris FlexNoC interconnect IP](#) for use in its integrated chipsets.

"FlexNoC enables DisplayLink to build on a predictable infrastructure for high frequency interconnects required in our SoC family."

DisplayLink-logo-LinkedIn-Post

*Ian Stacey, Vice President Silicon Engineering and Operations, **DisplayLink***

DisplayLink is a pioneer in enabling easy connectivity between monitors, virtual reality head mounted displays, and computing devices over standard interfaces such as USB, Ethernet, and wireless networks. DisplayLink technology powers products by leading companies like Dell, Lenovo, and Targus.

"FlexNoC enables DisplayLink to build on a predictable infrastructure for high frequency interconnects required in our SoC family," said Ian Stacey, Vice President Silicon Engineering and Operations at DisplayLink.

"DisplayLink's adoption of Arteris FlexNoC interconnect IP proves the time to market advantages when using Arteris IP technology," said K. Charles Janac, President and CEO of Arteris IP. "Arteris IP is happy to partner with DisplayLink as they revolutionize connectivity for high-bandwidth displays and monitors."

About DisplayLink

DisplayLink® (www.displaylink.com) develops hardware and software solutions to enable easy connectivity between monitors, virtual reality HMDs and computing devices over standard interfaces such as USB, Ethernet, and wireless networks. DisplayLink's enterprise technology increases productivity and ease-of-use in the multi-display workspace and is deployed to millions of users through globally branded PC products. DisplayLink's XR solution enables a fully immersive, cable-free experience for virtual reality users. Follow DisplayLink on [YouTube](#), [LinkedIn](#), [Facebook](#) and [Twitter](#).

About Arteris IP

Arteris IP provides [network-on-chip \(NoC\) interconnect IP](#) to accelerate system-on-chip (SoC) semiconductor assembly for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Baidu](#), [Mobileye](#), [Samsung](#), [Huawei / HiSilicon](#), [Toshiba](#) and [NXP](#). Arteris IP products include the [Ncore](#) cache coherent and [FlexNoC](#) non-coherent interconnect IP, the [CodaCache](#) standalone last level cache, and optional [Resilience Package \(ISO 26262 functional safety\)](#), [FlexNoC AI Package](#), and [PIANO automated timing closure](#) capabilities. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

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