



Arteris® IP FlexNoC® Interconnect Licensed by Achronix for New Speedster®7t FPGA family

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Network-on-chip (NoC) interconnect enables ASIC-like performance for Speedster7t FPGA family

CAMPBELL, Calif. — June 18, 2019 — Arteris IP, the world's leading supplier of innovative, silicon-proven [network-on-chip \(NoC\) interconnect](#) intellectual property, today announced that Achronix Semiconductor Corporation has licensed [Arteris FlexNoC interconnect IP](#) for use in its new [Speedster7t FPGA family](#) – based on a new, highly optimized architecture – that goes beyond traditional FPGA solutions featuring ASIC-like performance, FPGA adaptability and enhanced functionality to streamline designs.

"Our new Speedster7t FPGA family requires extremely high on-chip bandwidth and advanced dataflow arbitration to make possible ASIC-class machine learning processing. The Arteris FlexNoC IP is the optimal interconnect to meet these demands, especially with the advanced process technology nodes and multi-gigahertz frequencies we are dealing with."

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Steve Mensor, Vice President of Marketing, **Achronix**

Achronix, headquartered in Silicon Valley, specializes in FPGA-based hardware accelerator devices and high-performance embedded FPGA (eFPGA) IP that serve a broad range of computing, networking, and storage systems for interface-protocol bridging/switching, algorithm acceleration, and high-speed packet-processing applications. The company's latest Speedster7t FPGA family is specifically designed for Artificial Intelligence / Machine Learning (AI/ML) and high-bandwidth workloads, featuring a revolutionary new 2D NoC and a high-density array of new machine learning processors (MLP).

"Our new Speedster7t FPGA family requires extremely high on-chip bandwidth and advanced dataflow arbitration to make possible ASIC-class machine learning processing," said Steve Mensor, vice president of marketing at Achronix. "The Arteris FlexNoC IP is the optimal interconnect to meet these demands, especially with the advanced process technology nodes and multi-gigahertz frequencies we are dealing with."

"Achronix's choice of Arteris FlexNoC is proof of our IP's ability to enable the highest possible bandwidth dataflow using the latest process nodes running at extremely high frequencies," said K. Charles Janac, president and CEO of Arteris IP. "Arteris IP is the only IP company continually providing unique interconnect technologies that accelerate the development of these types of complex machine learning and artificial intelligence chips."

About Achronix Semiconductor Corporation

Achronix Semiconductor Corporation is a privately held, fabless semiconductor corporation based in Santa Clara, California and offers high-performance FPGA and embedded FPGA (eFPGA) solutions. Achronix's history is one of pushing the boundaries in the high-performance FPGA market. Achronix offerings include programmable FPGA fabrics, discrete high-performance and high-density FPGAs with hardwired system-level blocks, datacenter and HPC hardware accelerator boards, and best-in-class EDA software supporting all Achronix products. The company has sales offices and representatives in the United States, Europe, and China, and has a research and design office in Bangalore, India.

About Arteris IP

Arteris IP provides [network-on-chip \(NoC\) interconnect IP](#) to accelerate system-on-chip (SoC) semiconductor assembly for a wide range of applications from AI to automobiles, mobile phones, IoT, cameras, SSD controllers, and servers for customers such as [Baidu](#), [Mobileye](#), [Samsung](#), [Huawei / HiSilicon](#), [Toshiba](#) and [NXP](#). Arteris IP products include the [Ncore](#)® cache coherent and [FlexNoC](#)® non-coherent interconnect IP, the [CodaCache](#)® standalone last level cache, and optional [Resilience Package \(ISO 26262 functional safety\)](#), [FlexNoC AI Package](#), and [PIANO](#)® automated timing closure capabilities. Customer results obtained by using Arteris IP products include lower power, higher performance, more efficient design reuse and faster SoC development, leading to lower development and production costs. For more information, visit www.arteris.com or find us on LinkedIn at <https://www.linkedin.com/company/arteris>.

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