Arteris To Provide FlexGen Smart NoC IP In Next-Generation AMD AI Chiplet Designs

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Arteris' FlexGen smart NoC IP will be utilized by AMD in semiconductor designs to enable improved product performance and efficiency.

CAMPBELL, Calif., Aug. 04, 2025 (GLOBE NEWSWIRE) -- In a market reshaped by the compute demands of AI, Arteris, Inc. (Nasdaq: AIP), a leading provider of semiconductor system IP for accelerating system-on-chip (SoC) creation, today announced AMD (Nasdaq: AMD), a global leader in high-performance and adaptive computing, has licensed FlexGen network-on-chip (NoC) interconnect IP for its next generation of AI chiplet design. FlexGen, Arteris' smart NoC IP technology, will provide high-performance data transport in AMD chiplets powering AI across the company's broad portfolio which spans from data centers to edge and end devices.

The strategic combination and interoperability of Arteris' FlexGen NoC IP with the AMD Infinity Fabric™ interconnect underscores the increasing complexity of modern SoCs and chiplet-based architectures, which now require multiple highly specialized interconnects or NoCs to efficiently meet the demands of modern electronic systems.

"We are excited to collaborate and expand our relationship with AMD, a company recognized globally for its innovation in high performance computing," said K. Charles Janac, president and CEO of Arteris. "With modern chiplets each having between 5 and 20 interconnect networks for data transport, our FlexGen NoC IP will work hand in hand with AMD's Infinity Fabric to accelerate the performance and scalability required by today's most demanding and diverse applications. This latest engagement with AMD exemplifies the transformative impact of Arteris' NoC technology in delivering next-generation silicon solutions for a wide range of markets from the data center to the edge."

"AMD is driving innovations that scale AI from cloud to client by continually developing leadership computing technologies and best-in-class IP," said Mydung Pham, corporate vice president silicon design engineering at AMD. "Integrating Arteris' FlexGen NoC IP technology into a range of AMD chiplets, we can automate interconnect configuration and enable seamless connectivity among SoC components while strengthening the best end-to-end AI compute portfolio in the industry."

Arteris is an industry leader in flexible and configurable network-on-chip IP technology. FlexGen, Arteris' latest NoC IP innovation, is specifically designed to make designing SoCs more efficient and to optimize wire length, reduce latency, and improve power efficiency, addressing the communication and performance needs of increasingly complex multi-die and chiplet-based designs. FlexGen can be utilized as an independent interconnect solution or in combination with proprietary interconnect technology to accelerate design iterations and time to market schedules.

Arteris continues to revolutionize SoC and chiplet performance with innovative technology. FlexGen smart NoC IP leverages AI automation to enhance design productivity through streamlined iteration cycles, and position companies to address the complexities of today's computing systems while dreaming up what comes next. Learn more at arteris.com/FlexGen.

About Arteris

Arteris is a global leader in system IP used in semiconductors to accelerate the creation of high-performance, power-efficient silicon. Arteris network-on-chip (NoC) interconnect IP and system-on-chip (SoC) integration automation software are used by the world's top semiconductor and technology companies to improve overall performance, engineering productivity, reduce risk, lower costs, and bring complex designs to market faster. Learn more at arteris.com.

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